

TOP SECRET 67092860

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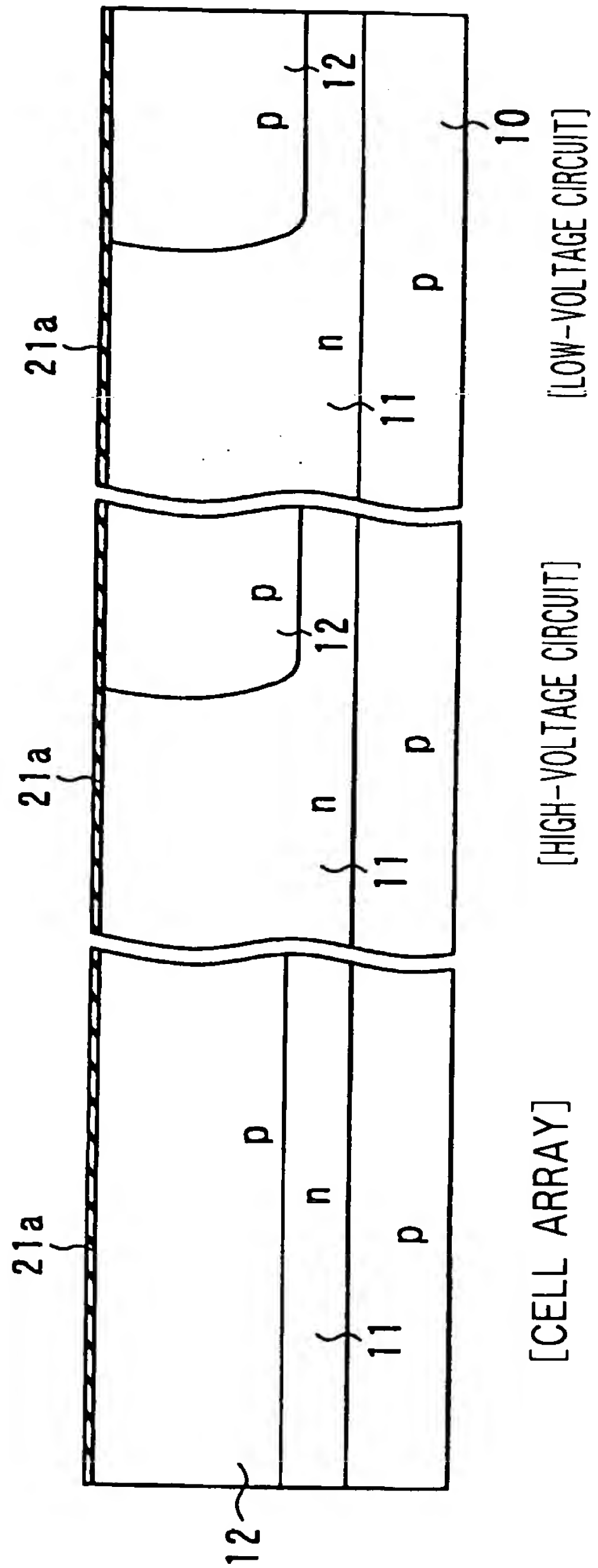


FIG. 1

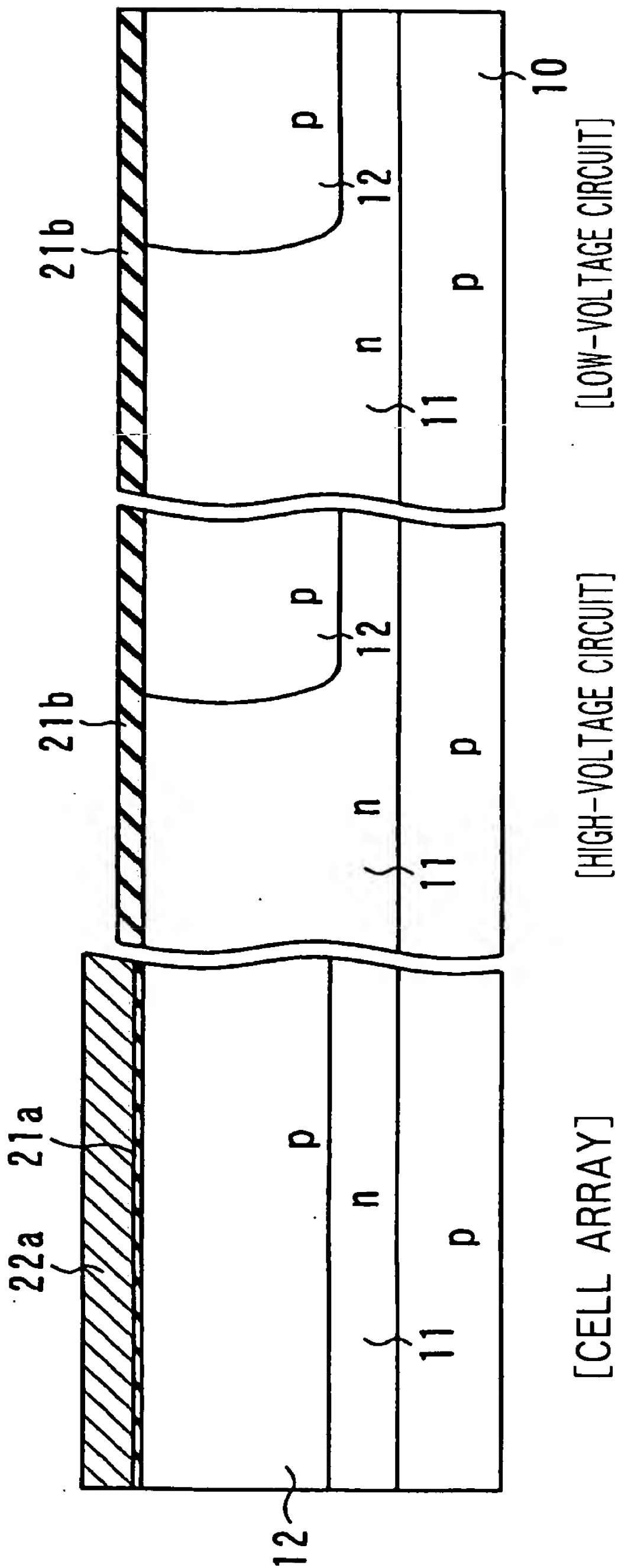


FIG. 2

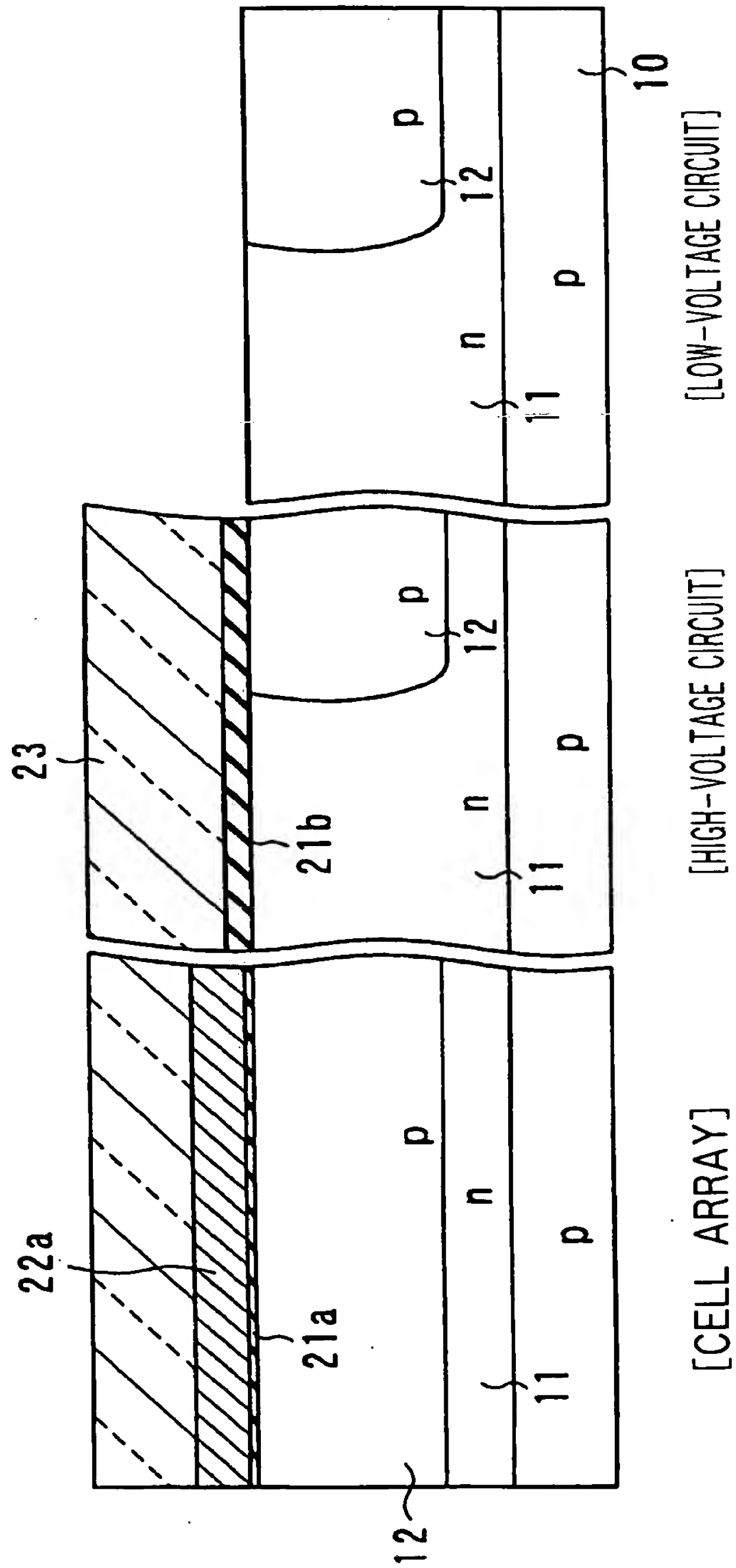


FIG. 3

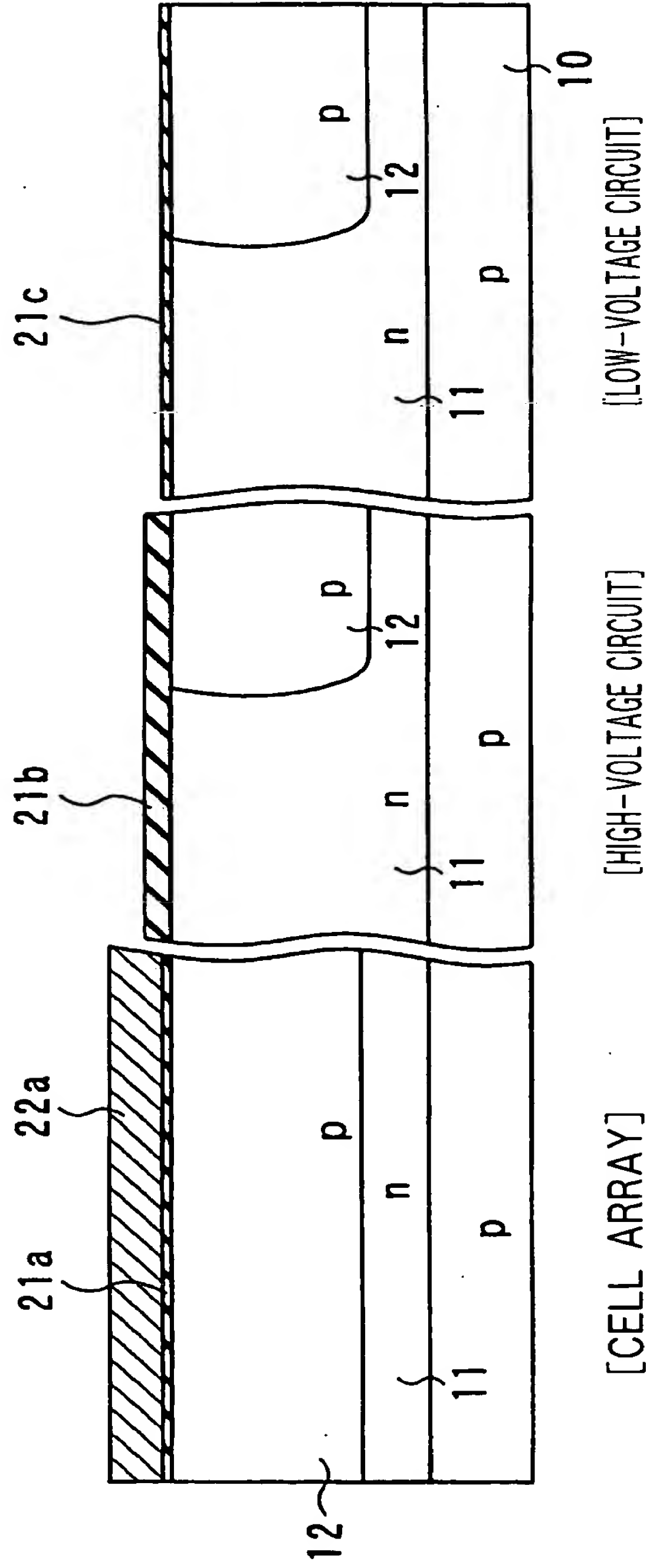


FIG. 4

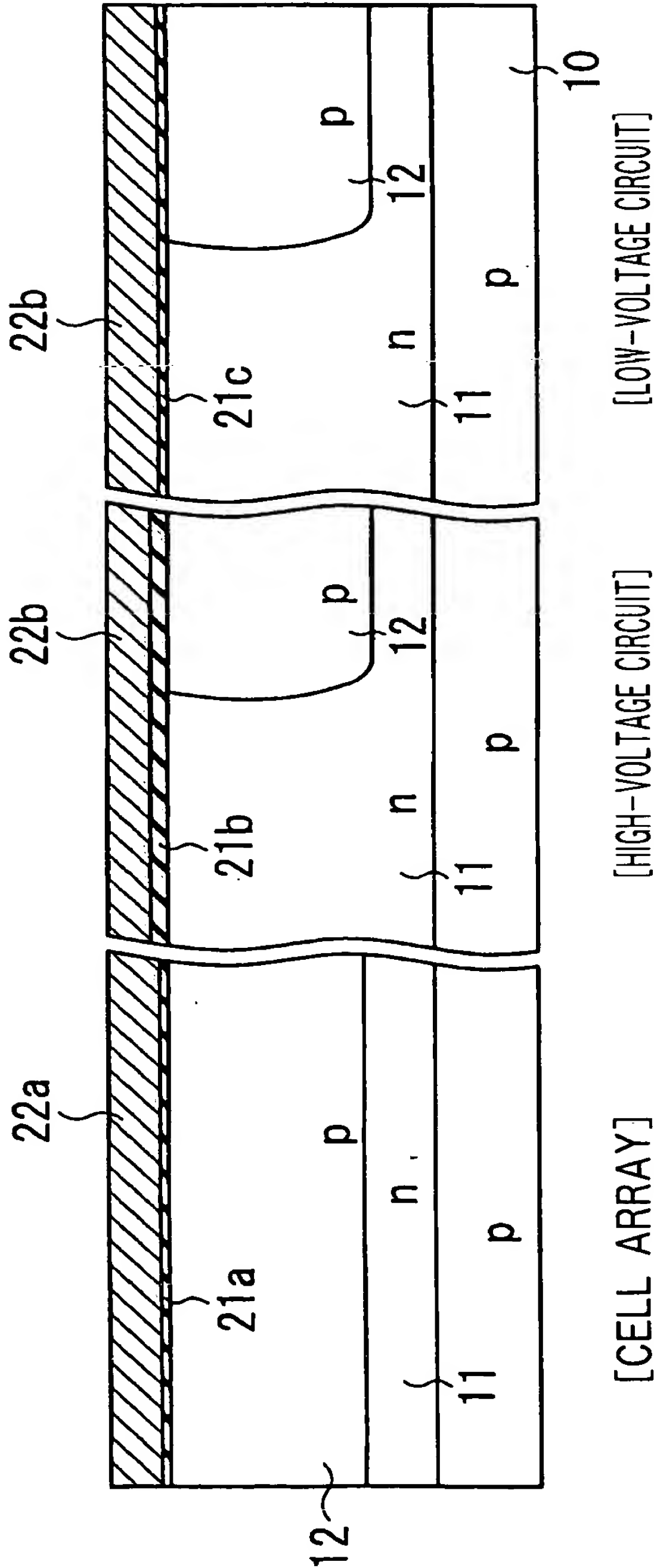


FIG. 5

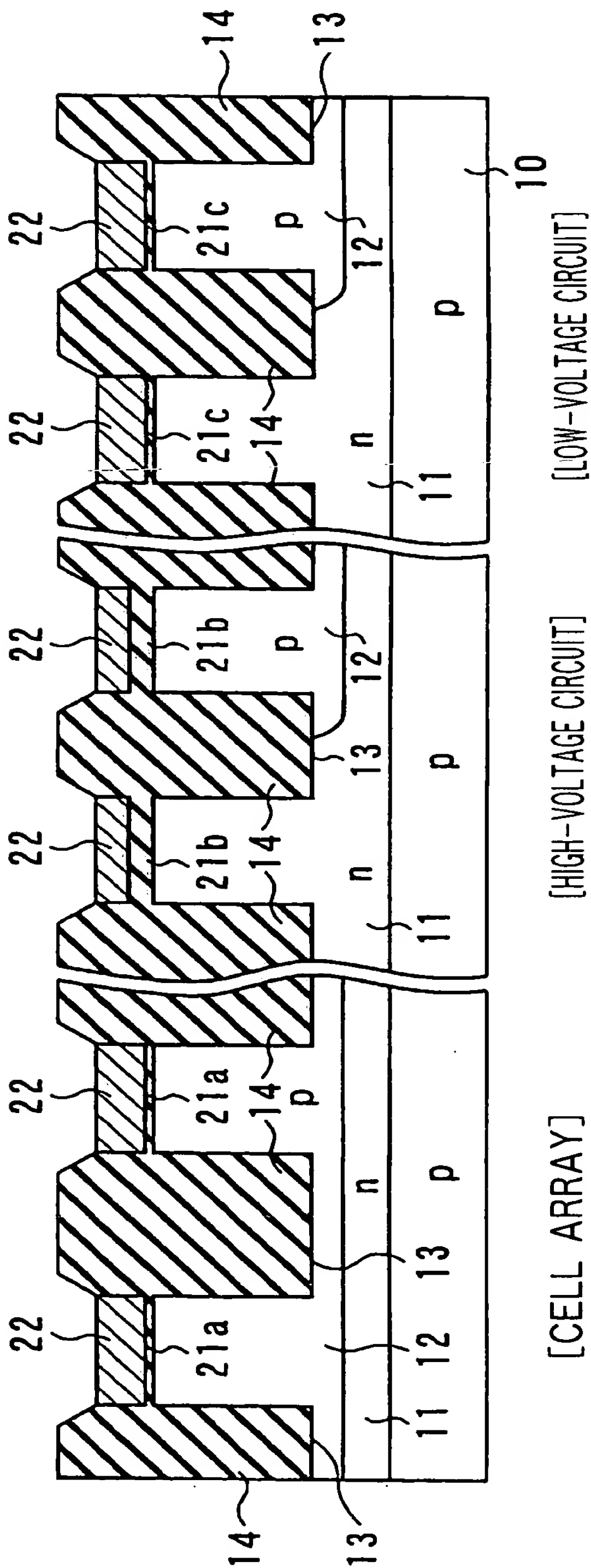


FIG. 6

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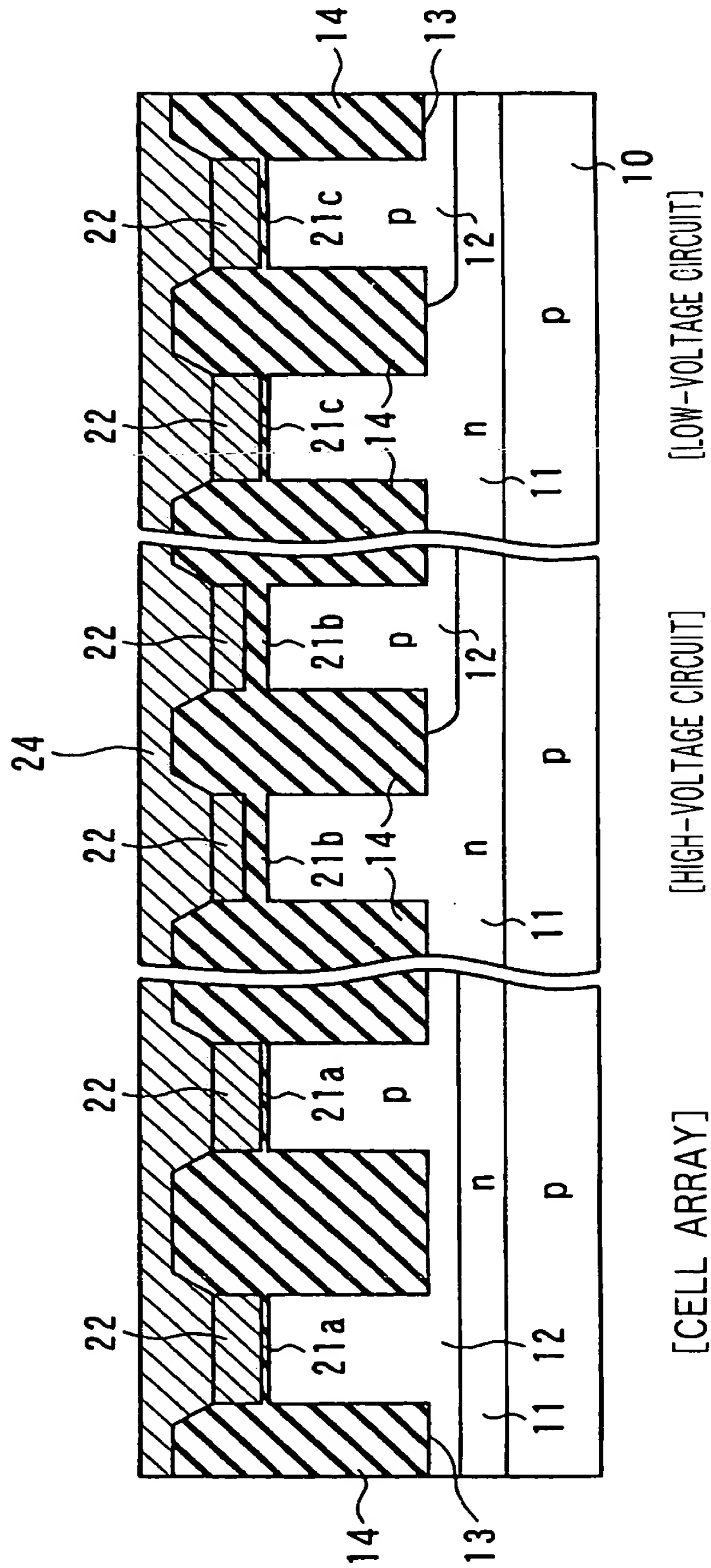


FIG. 7

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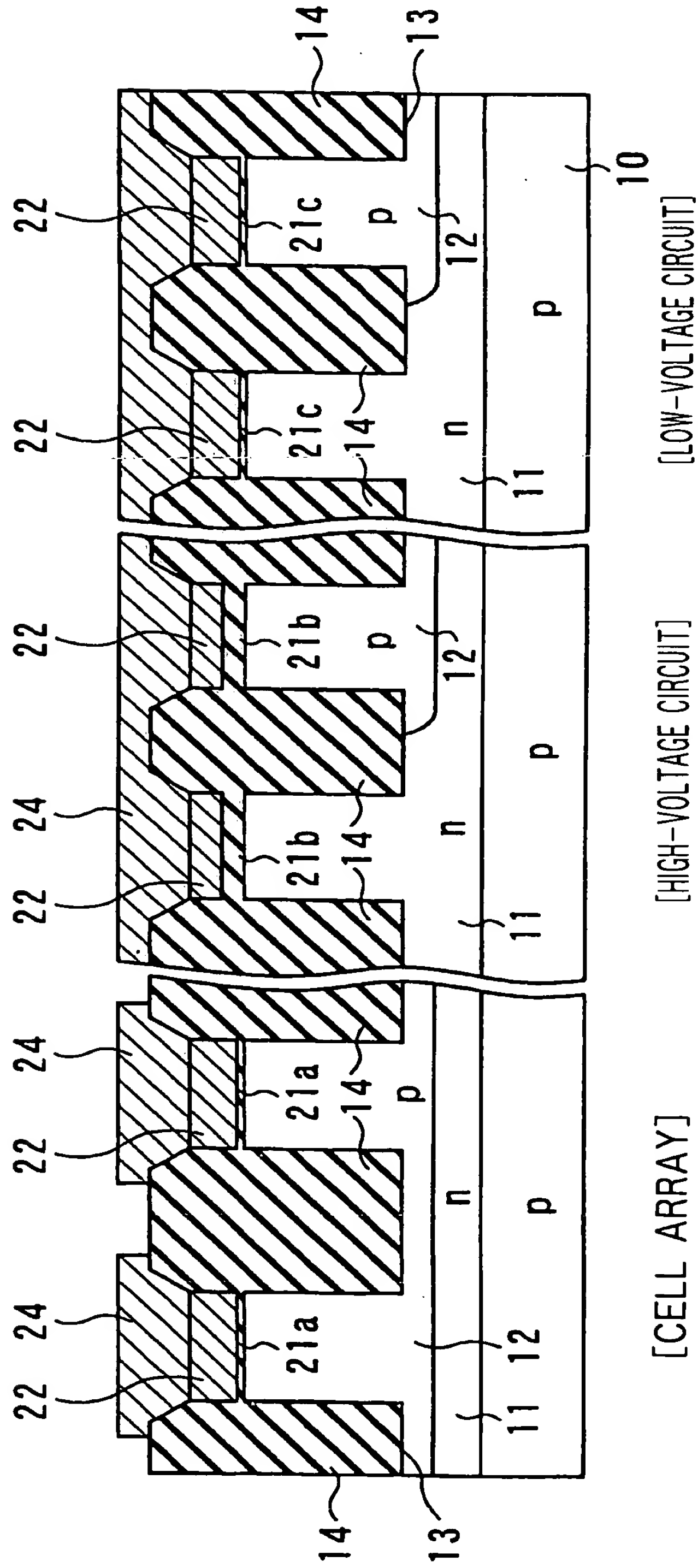


FIG. 9



FIG. 10

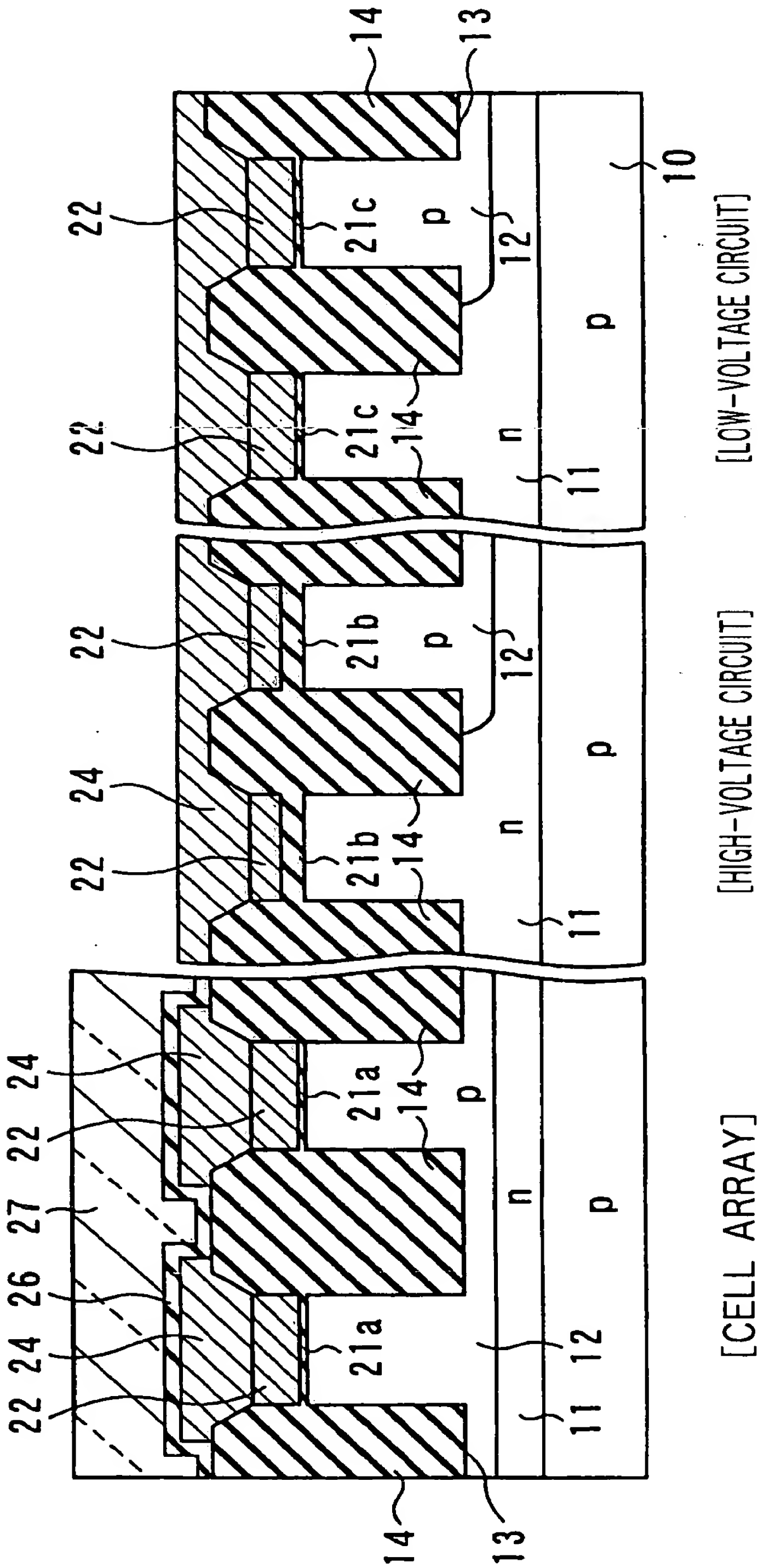


FIG. 11

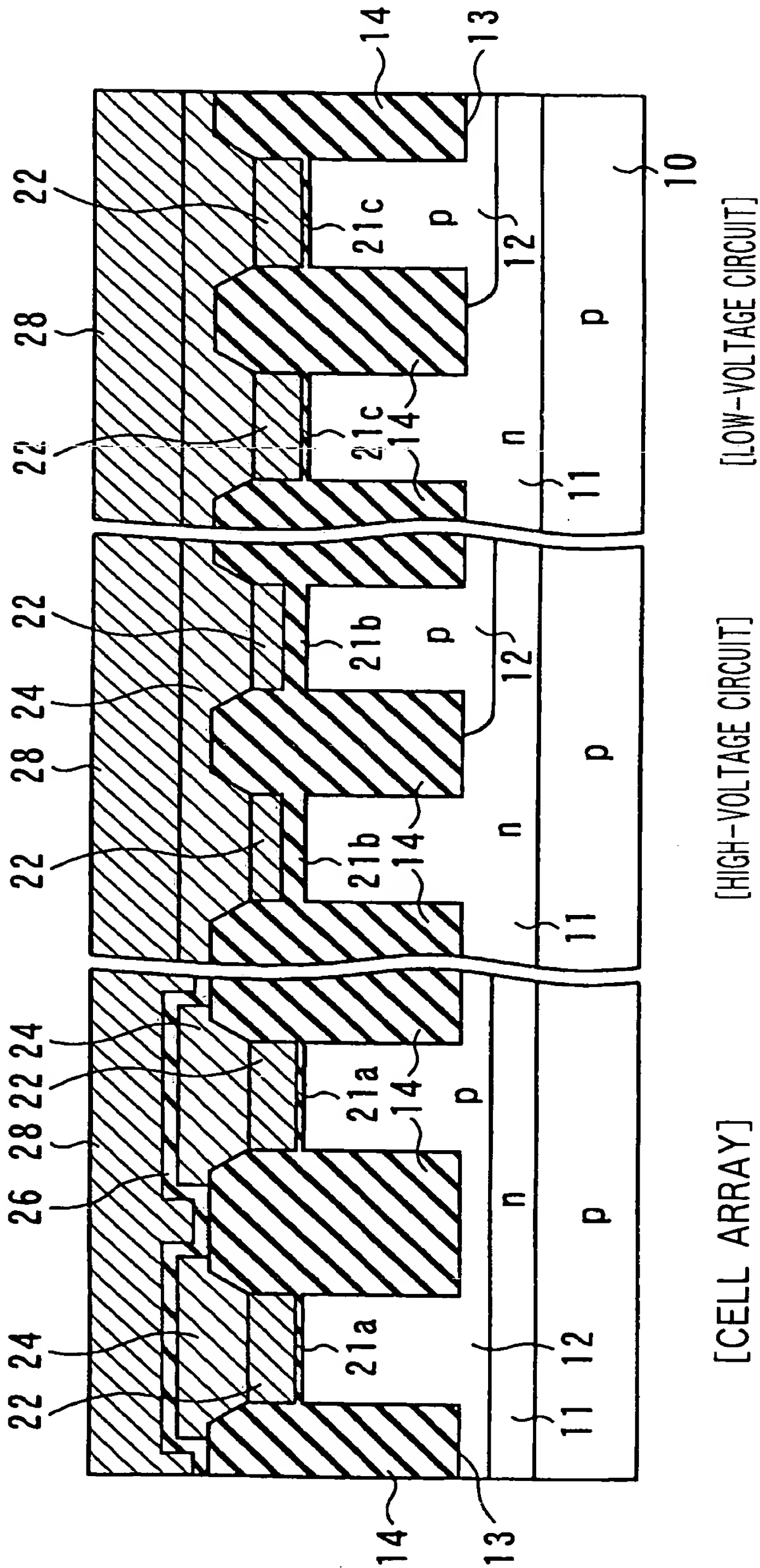


FIG. 12

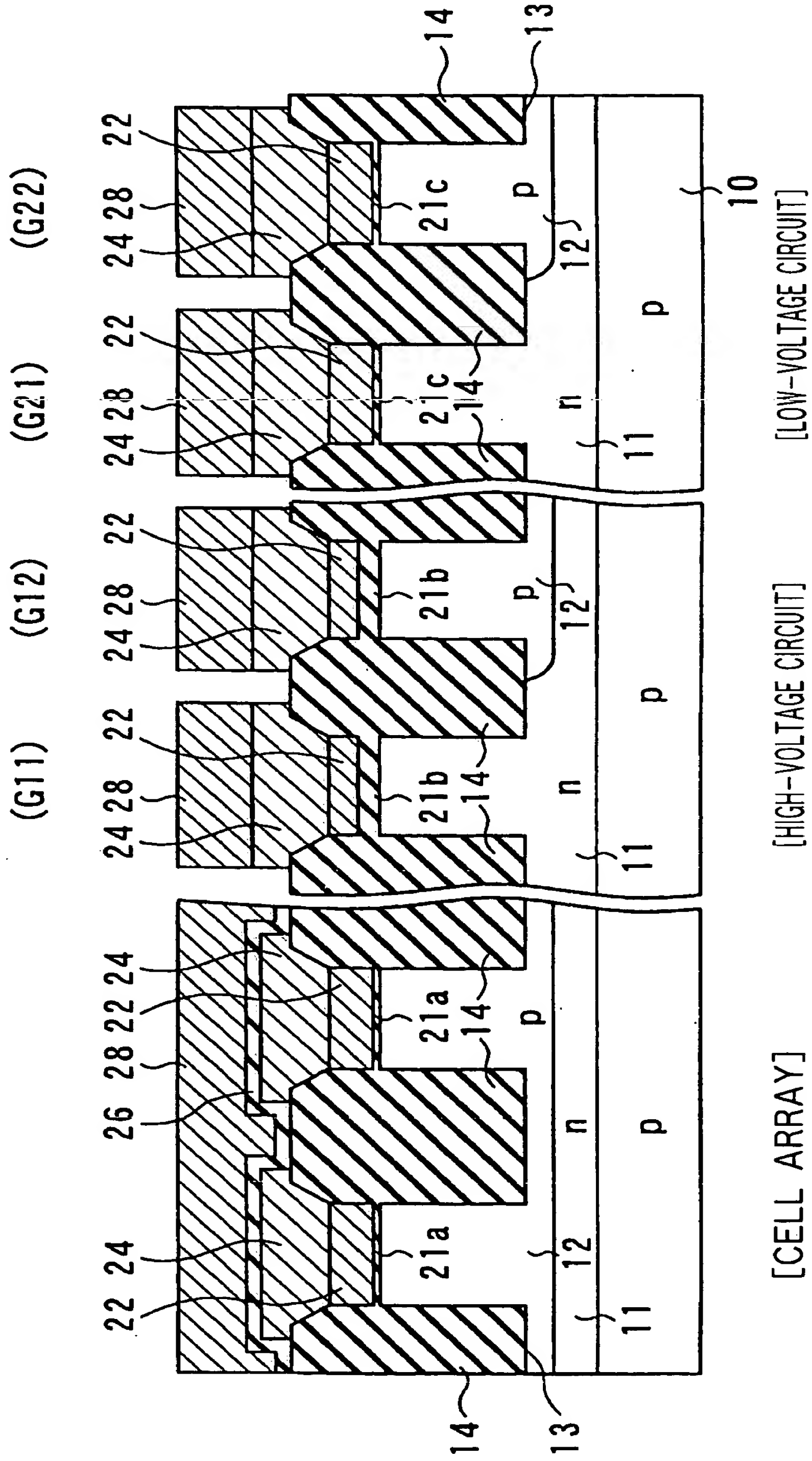


FIG. 13

FIG. 14

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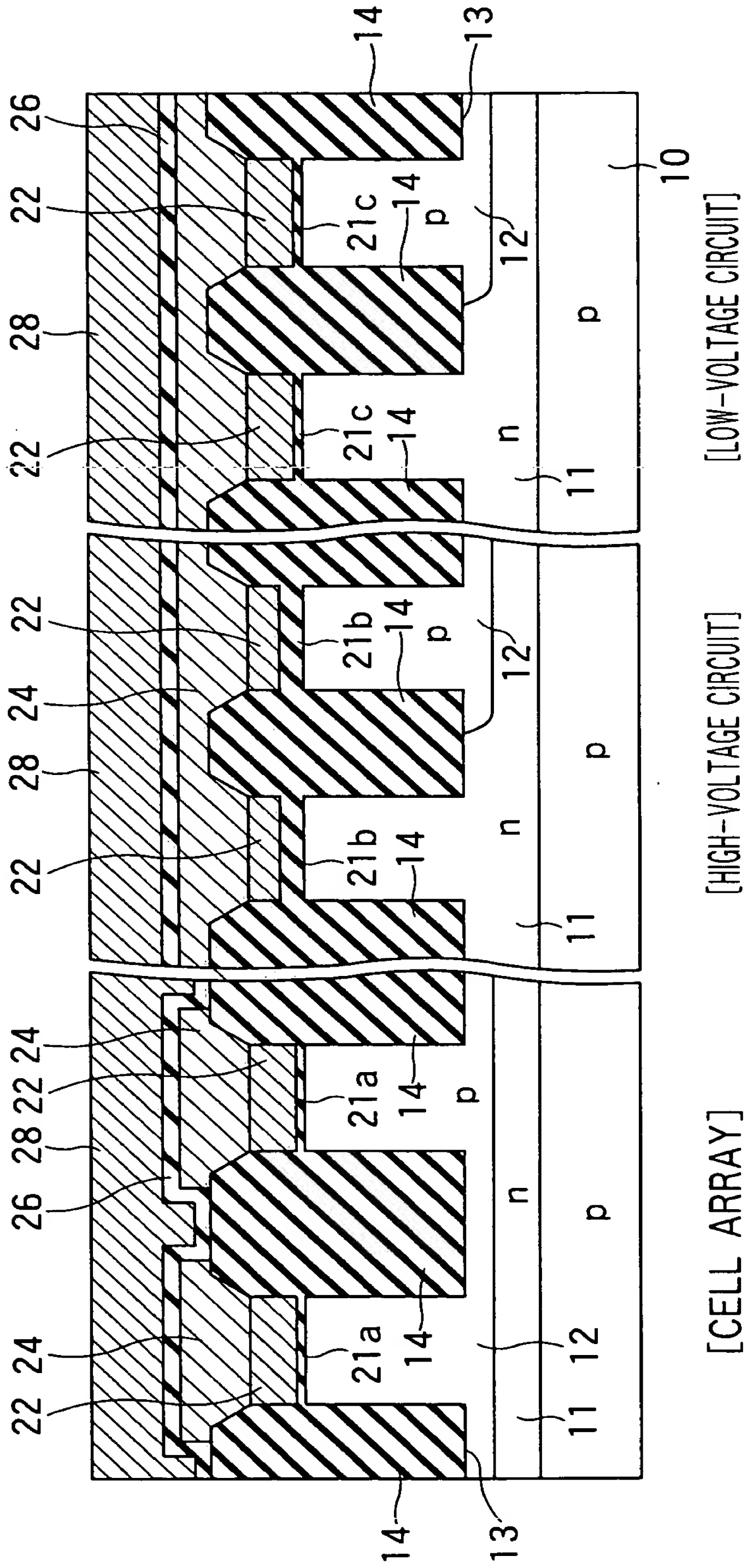


FIG. 14A

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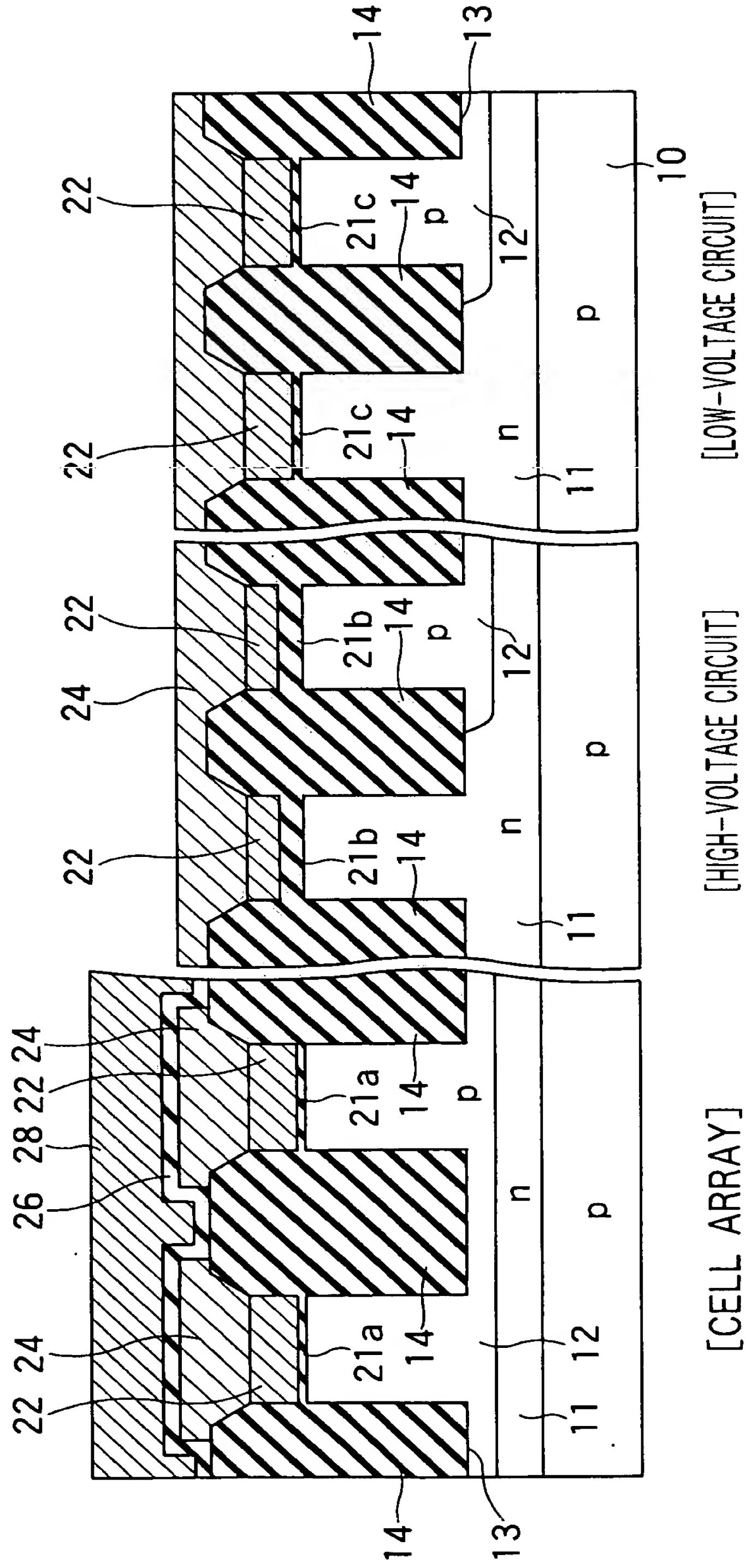


FIG. 14B

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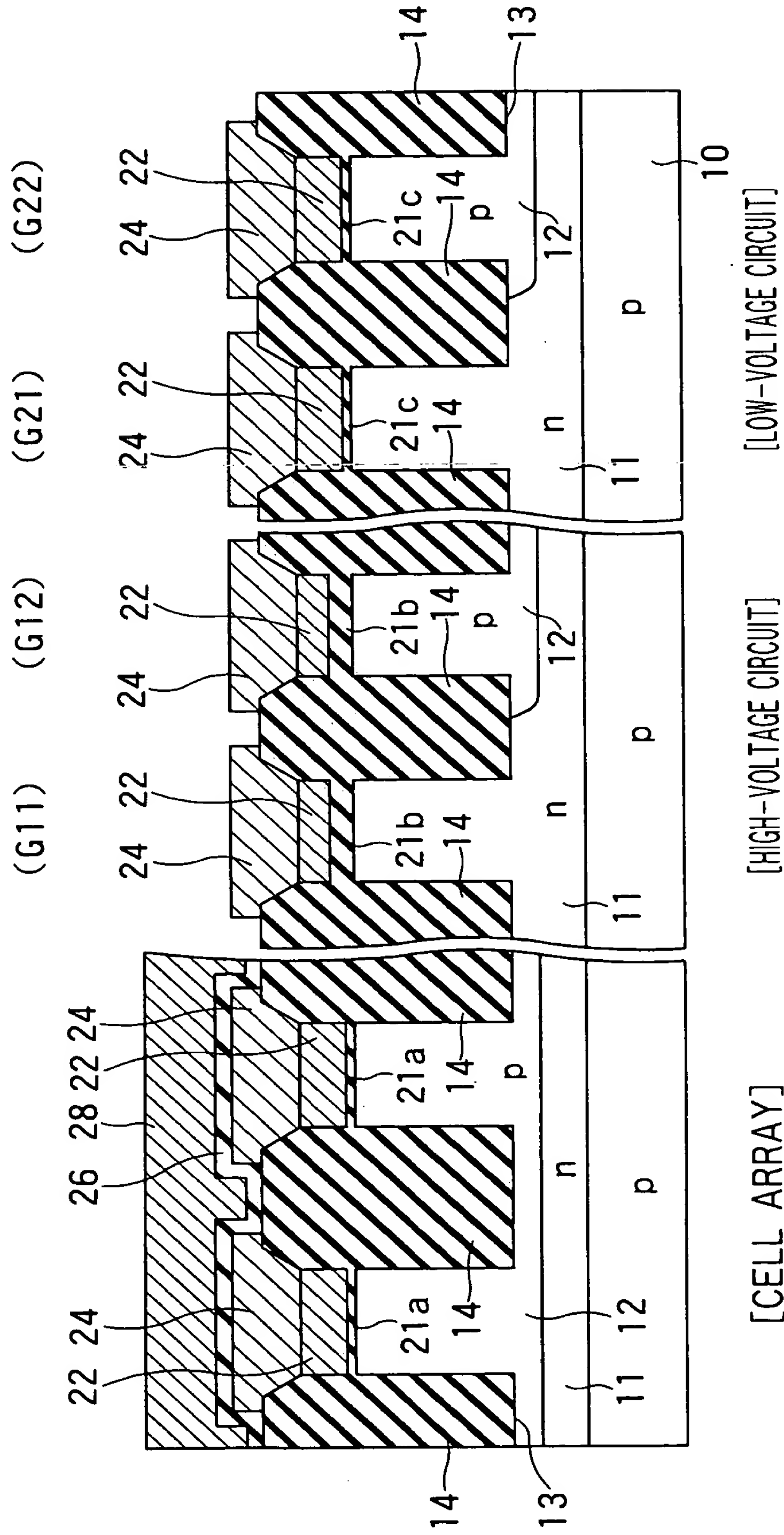


FIG. 14C

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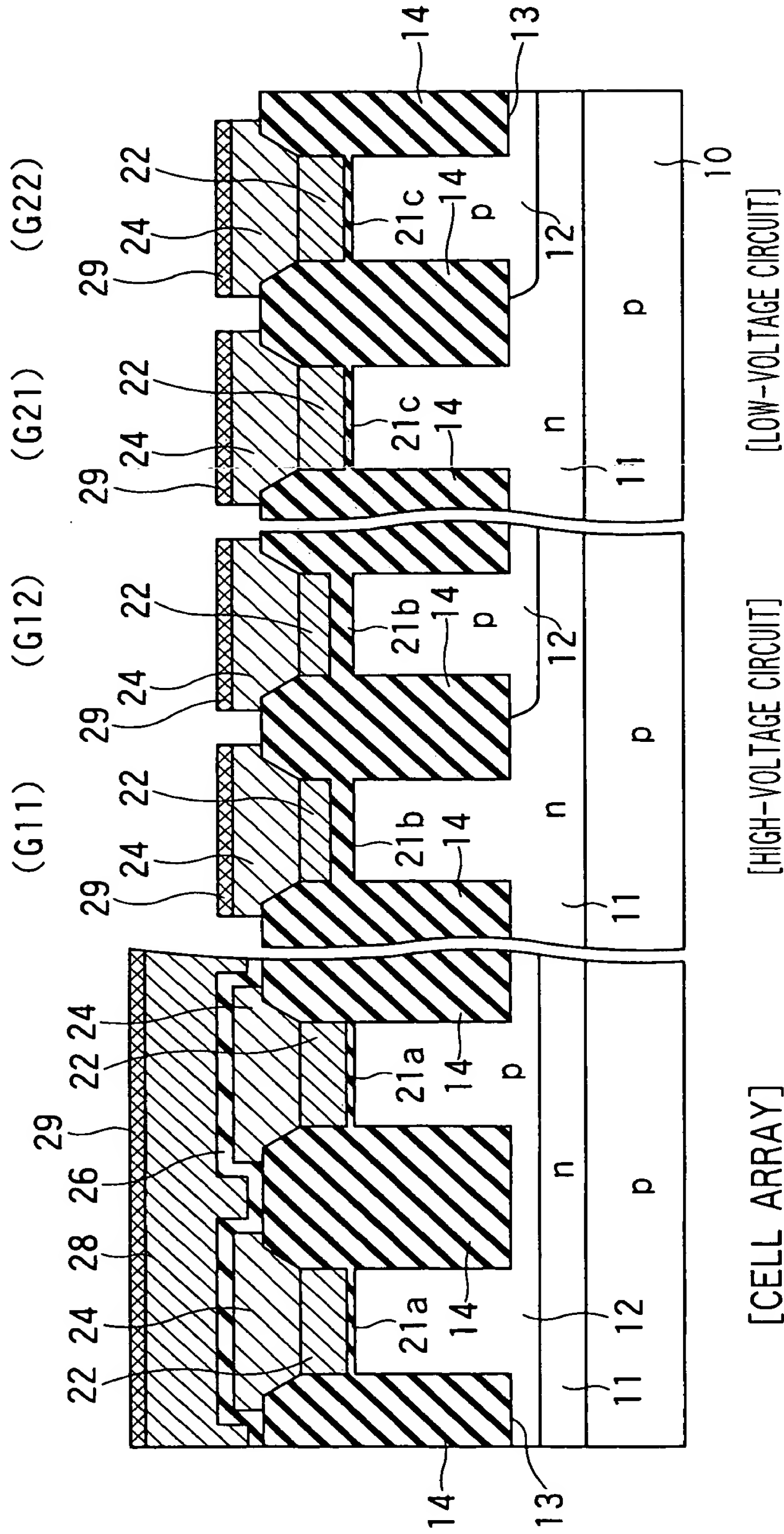
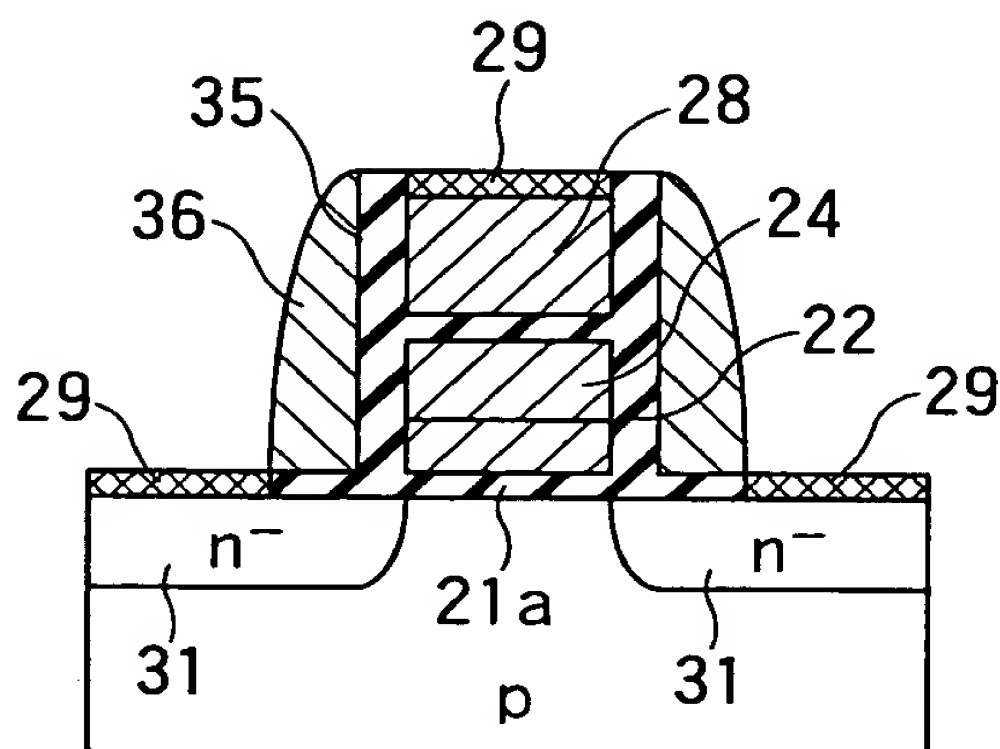
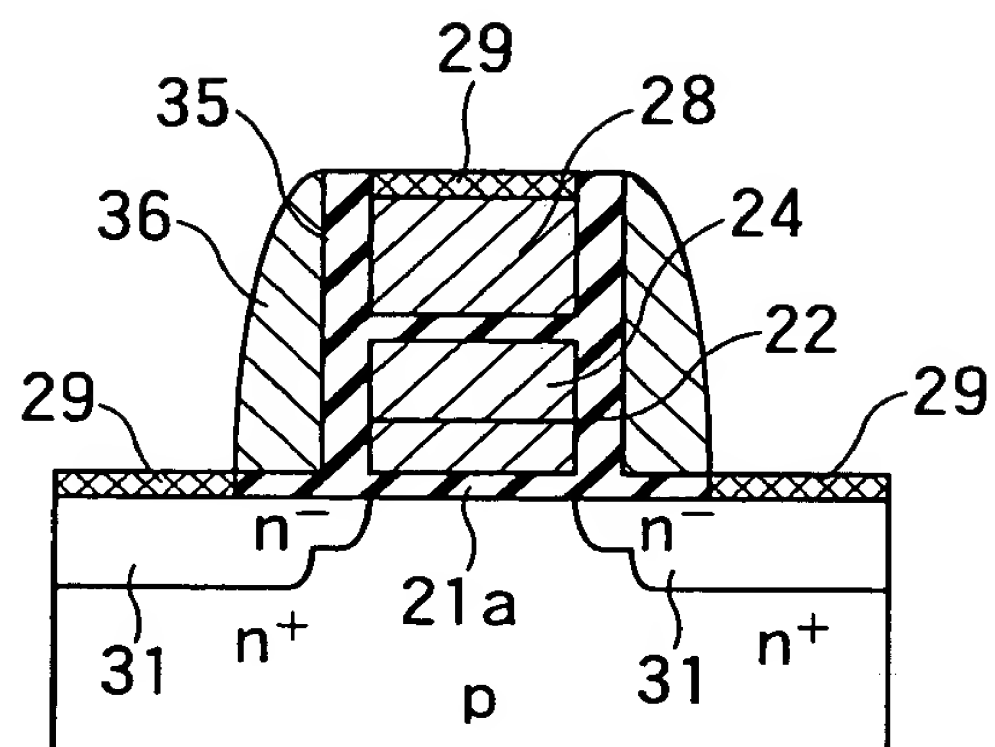


FIG. 14D

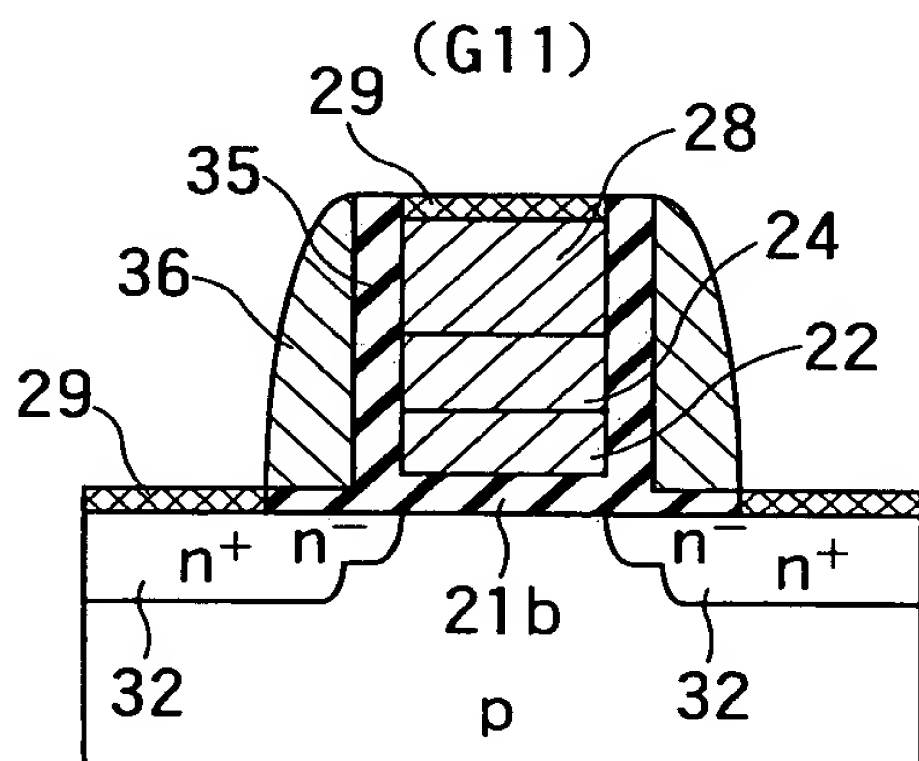
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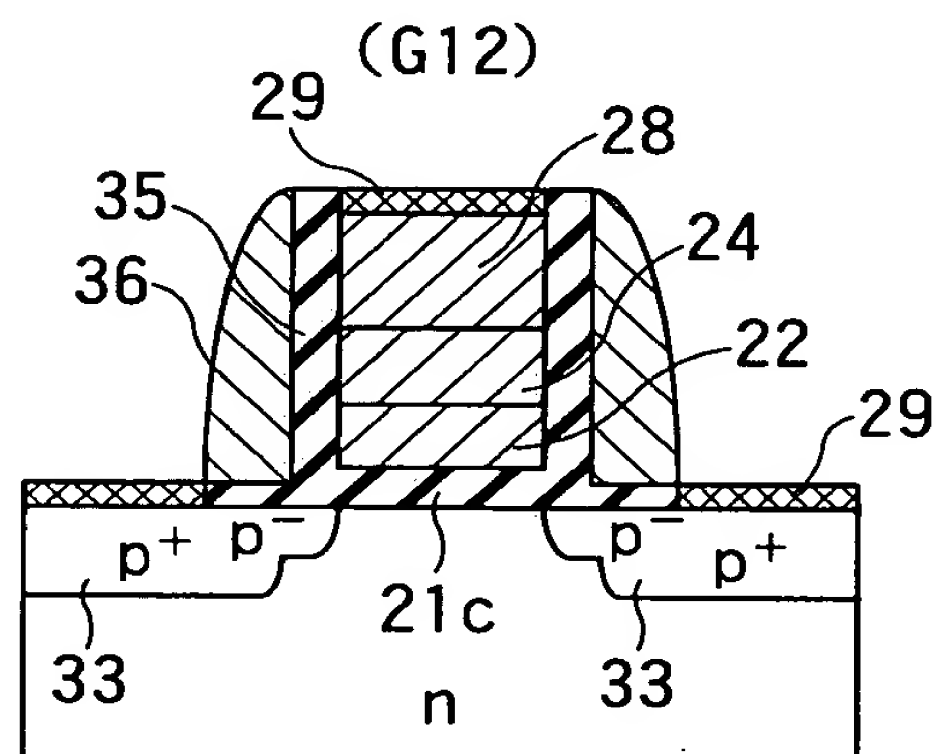
[MEMORY CELL]



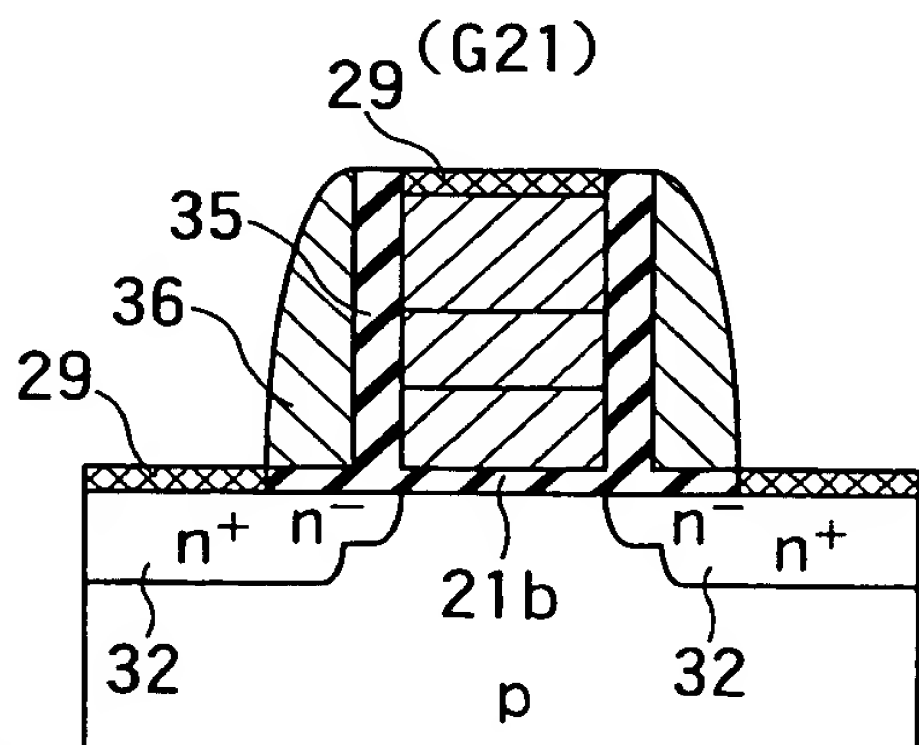
[MEMORY CELL]



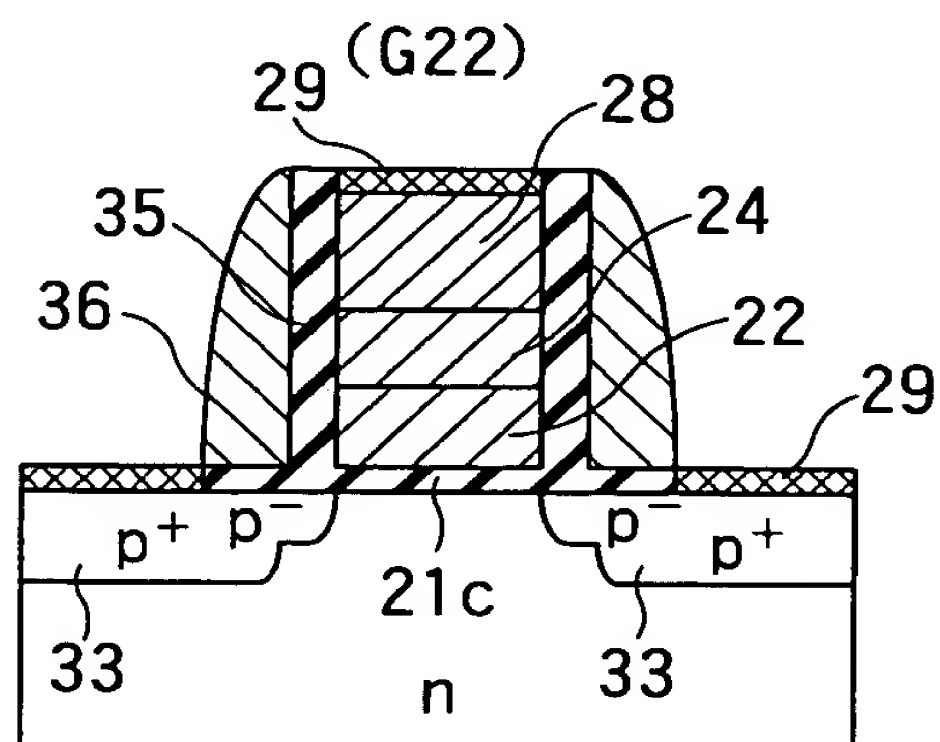
[HIGH-VOLTAGE NMOS]



[HIGH-VOLTAGE PMOS]



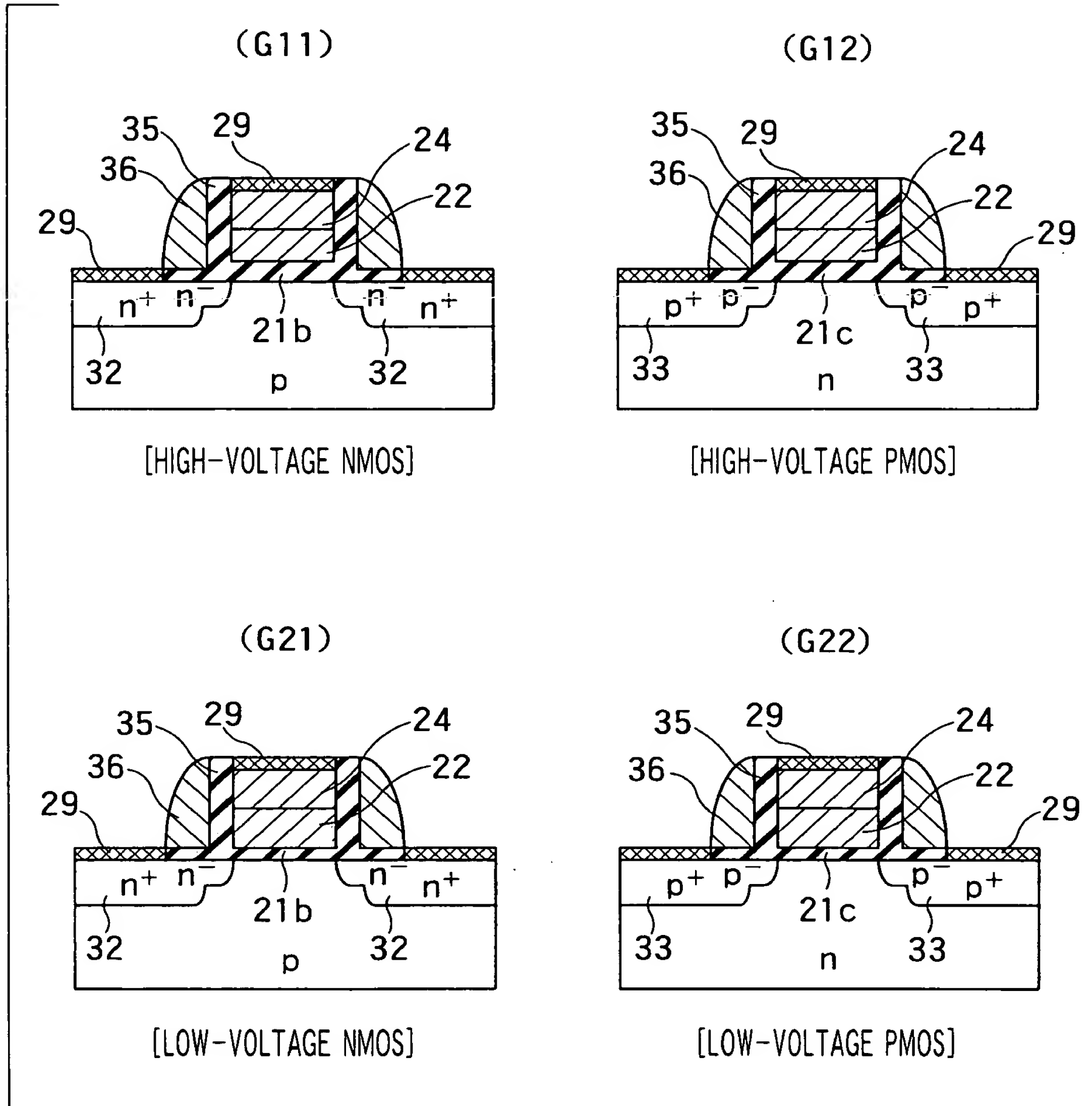
[LOW-VOLTAGE NMOS]



[LOW-VOLTAGE PMOS]

FIG. 15

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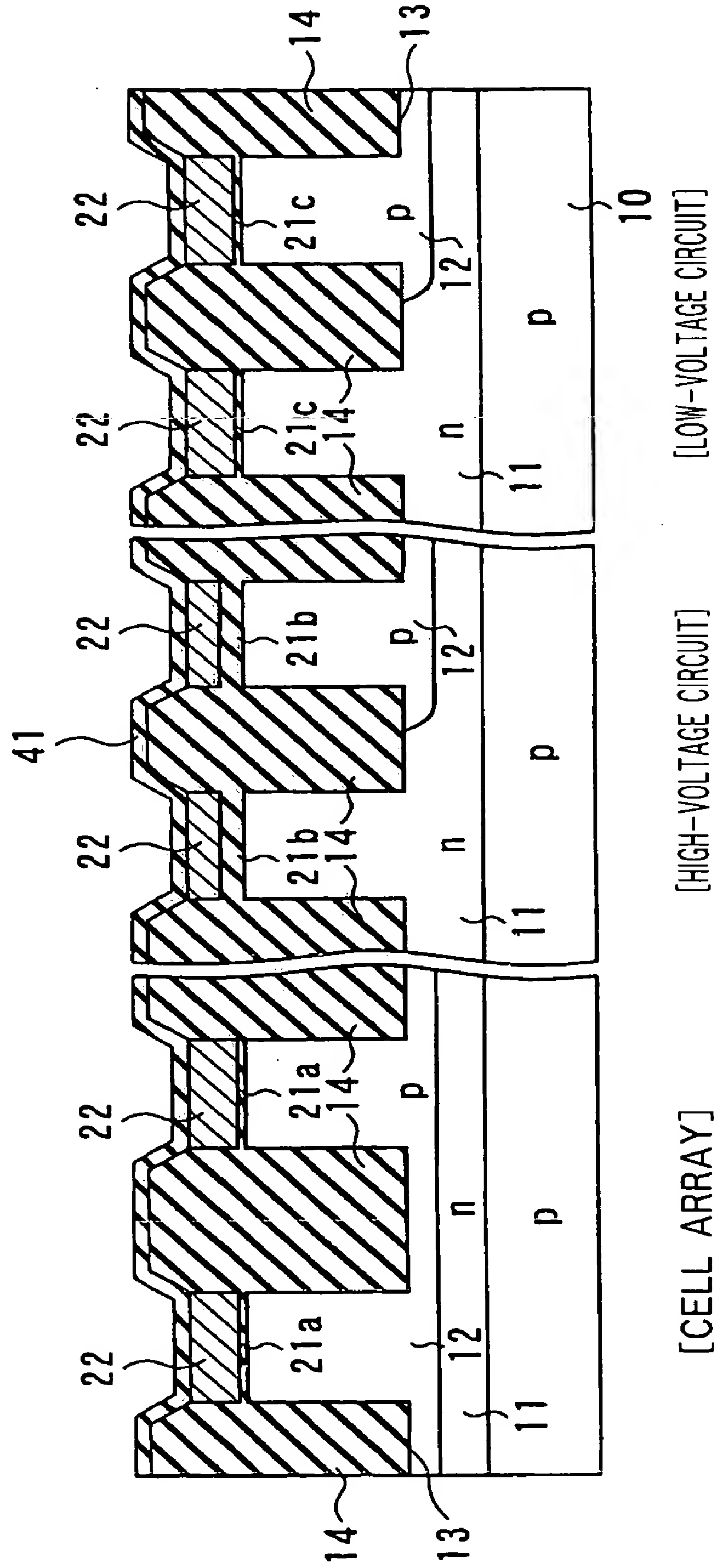


FIG. 16

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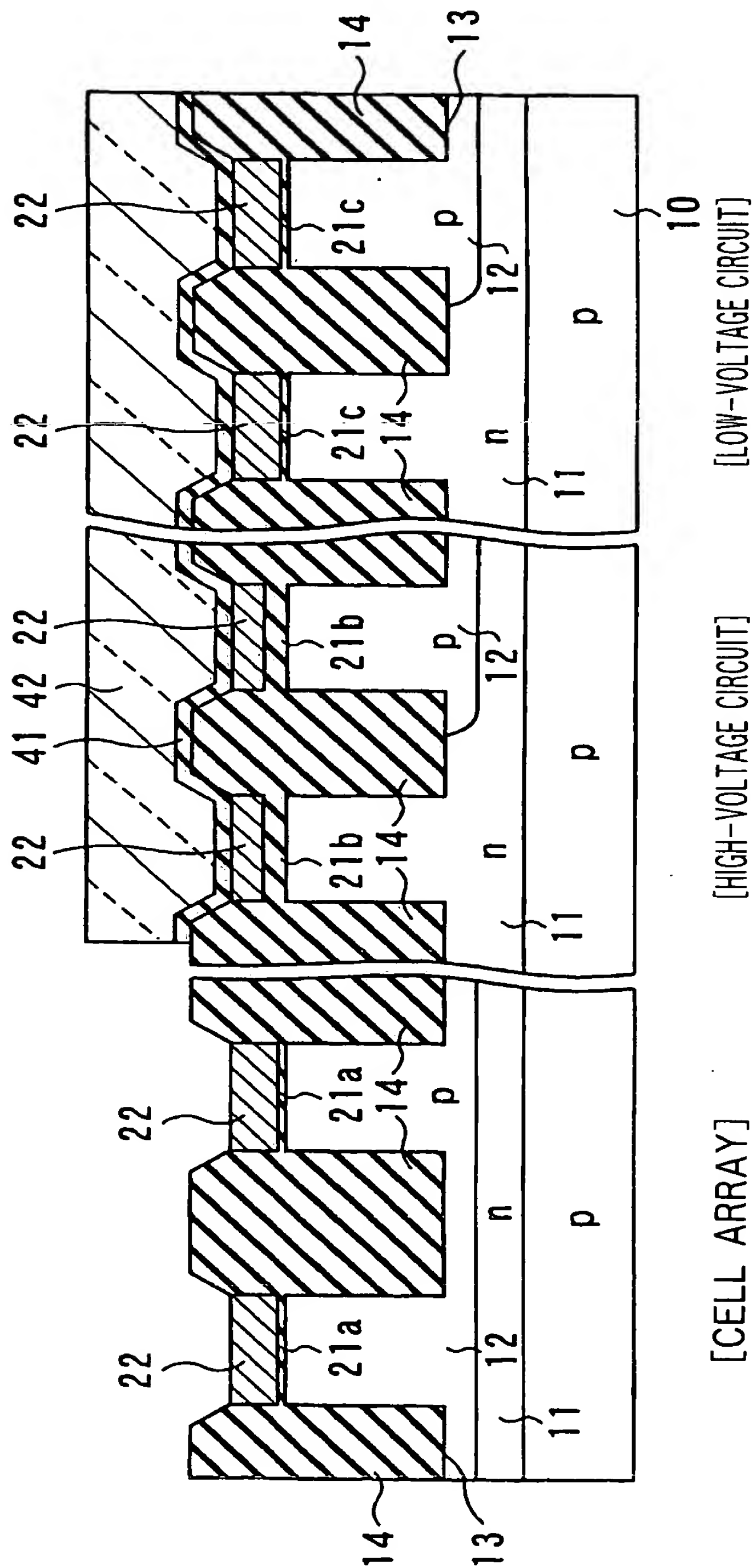


FIG. 17

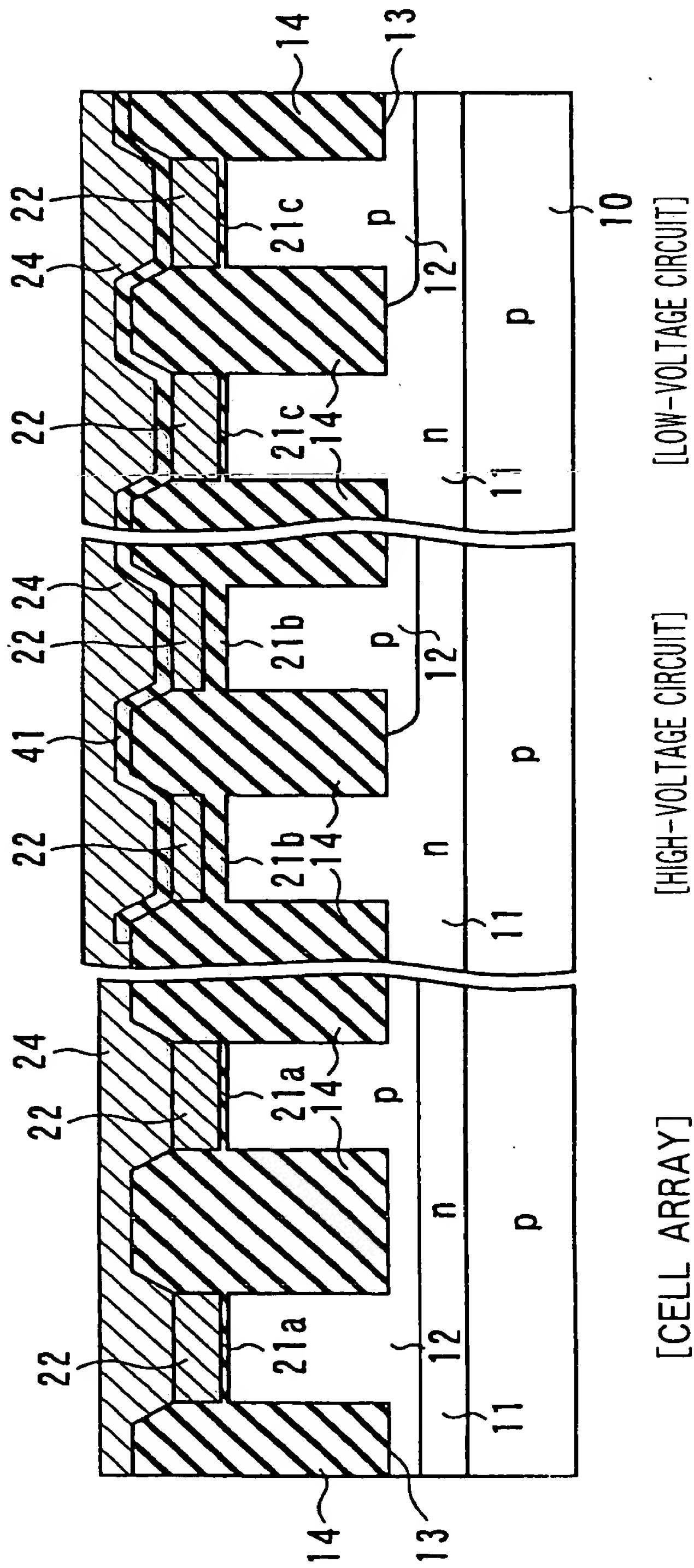


FIG. 18

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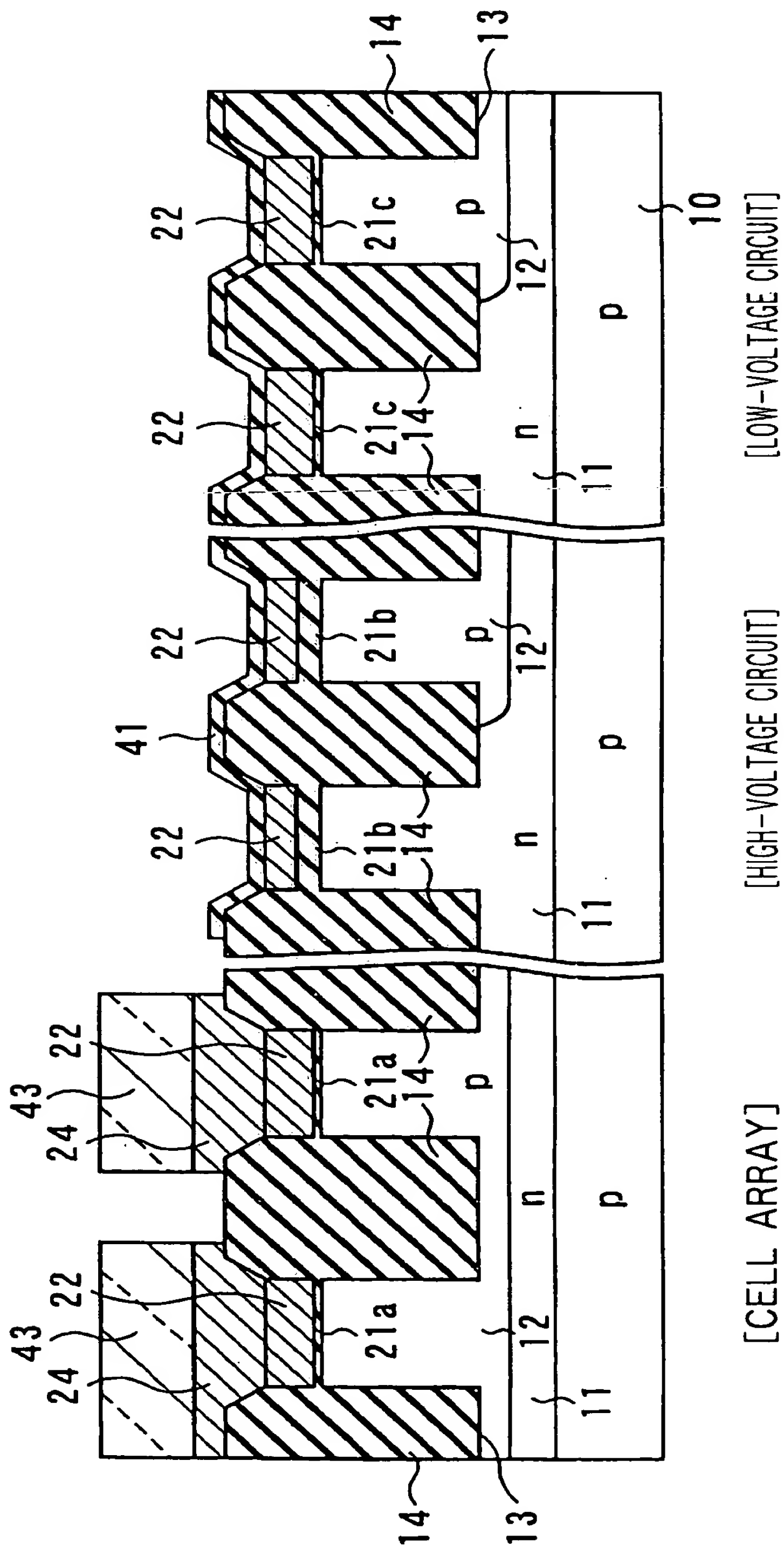


FIG. 19

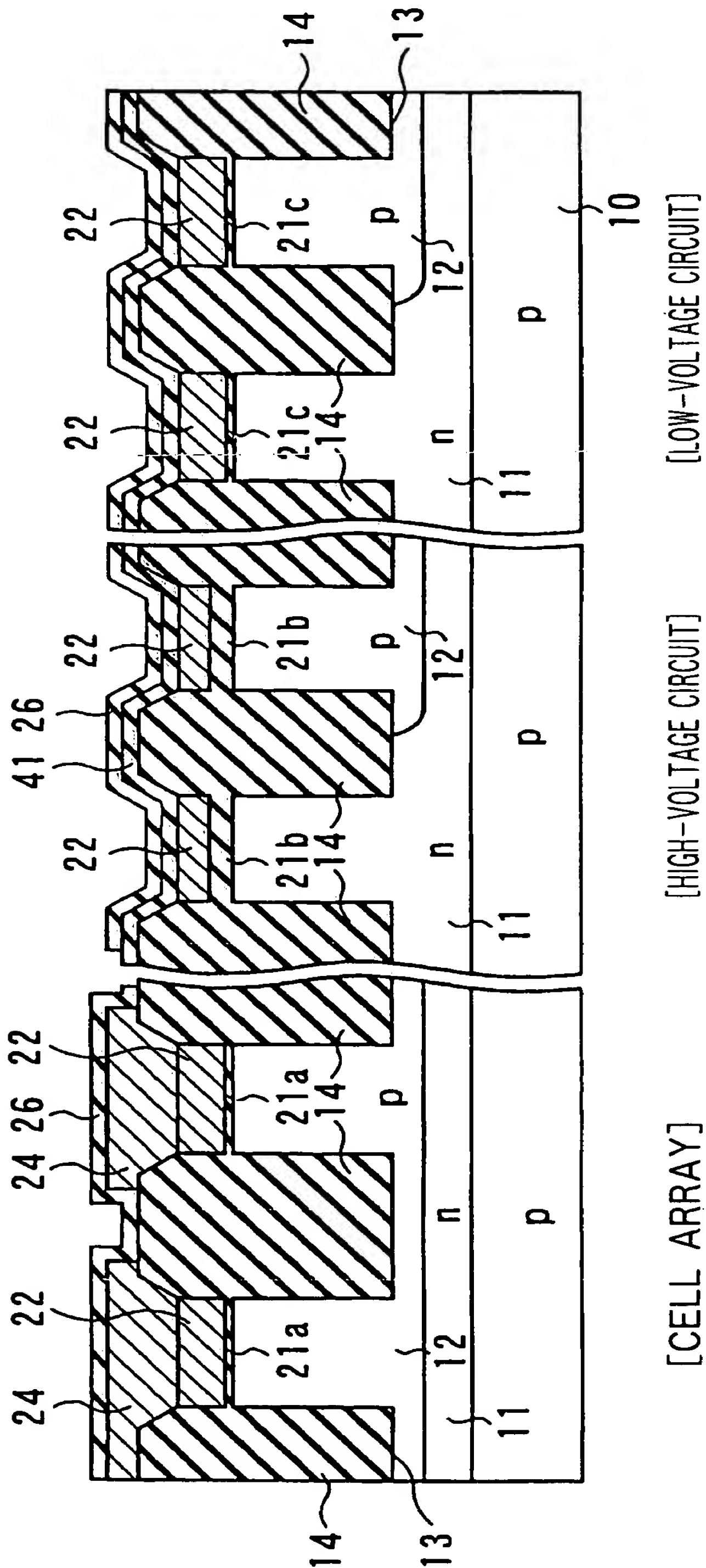


FIG. 20

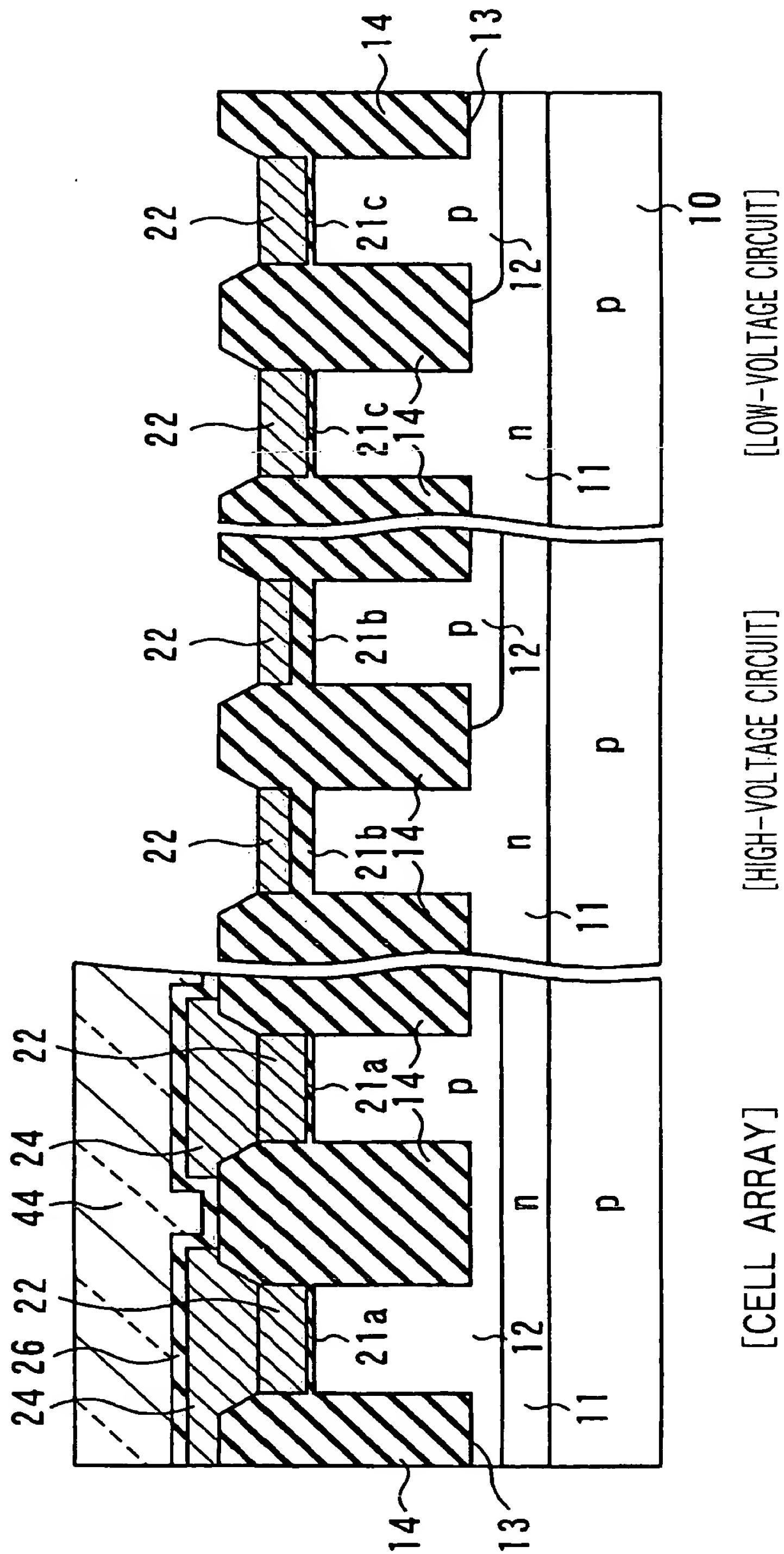


FIG. 21

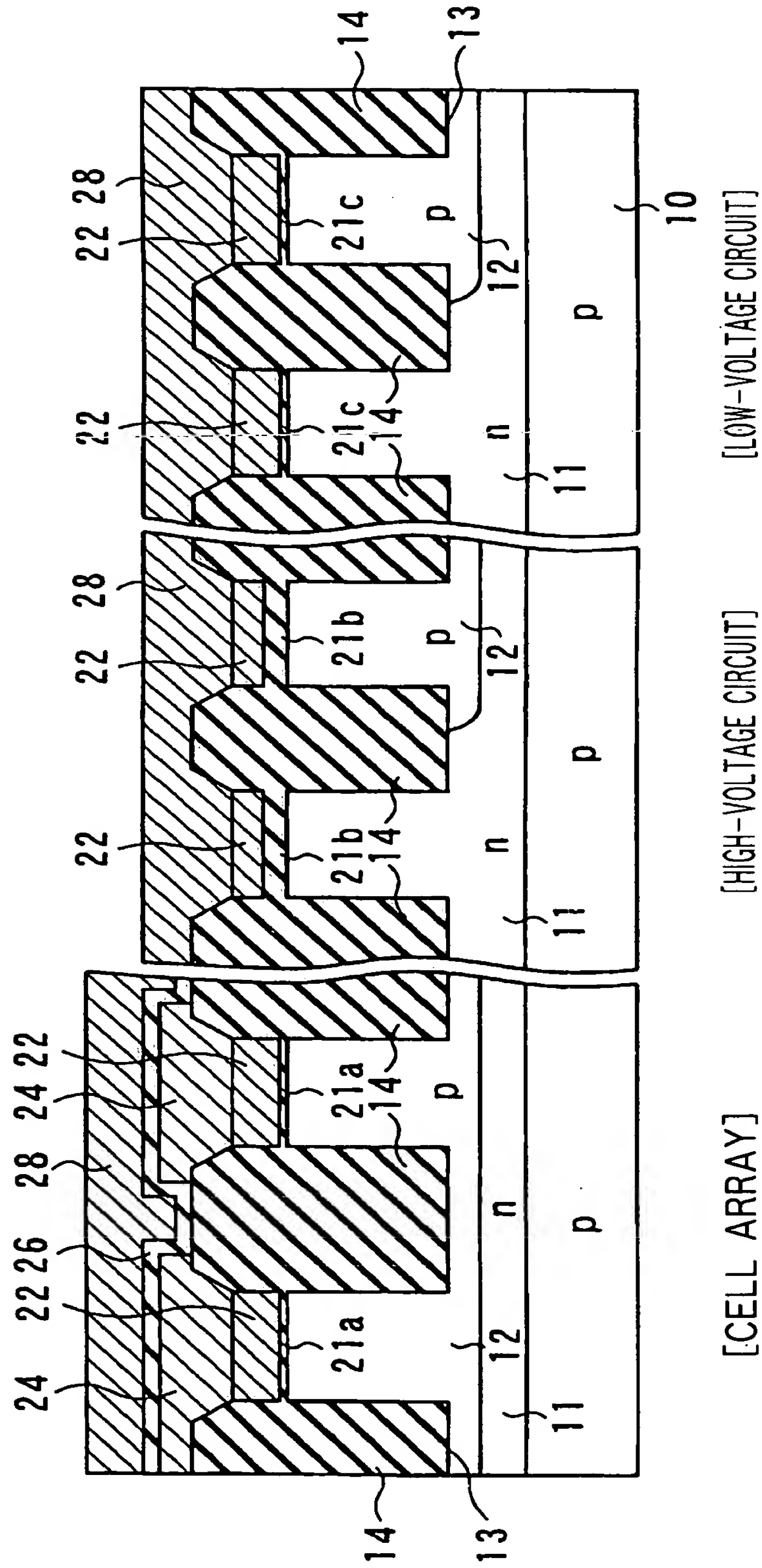


FIG. 22

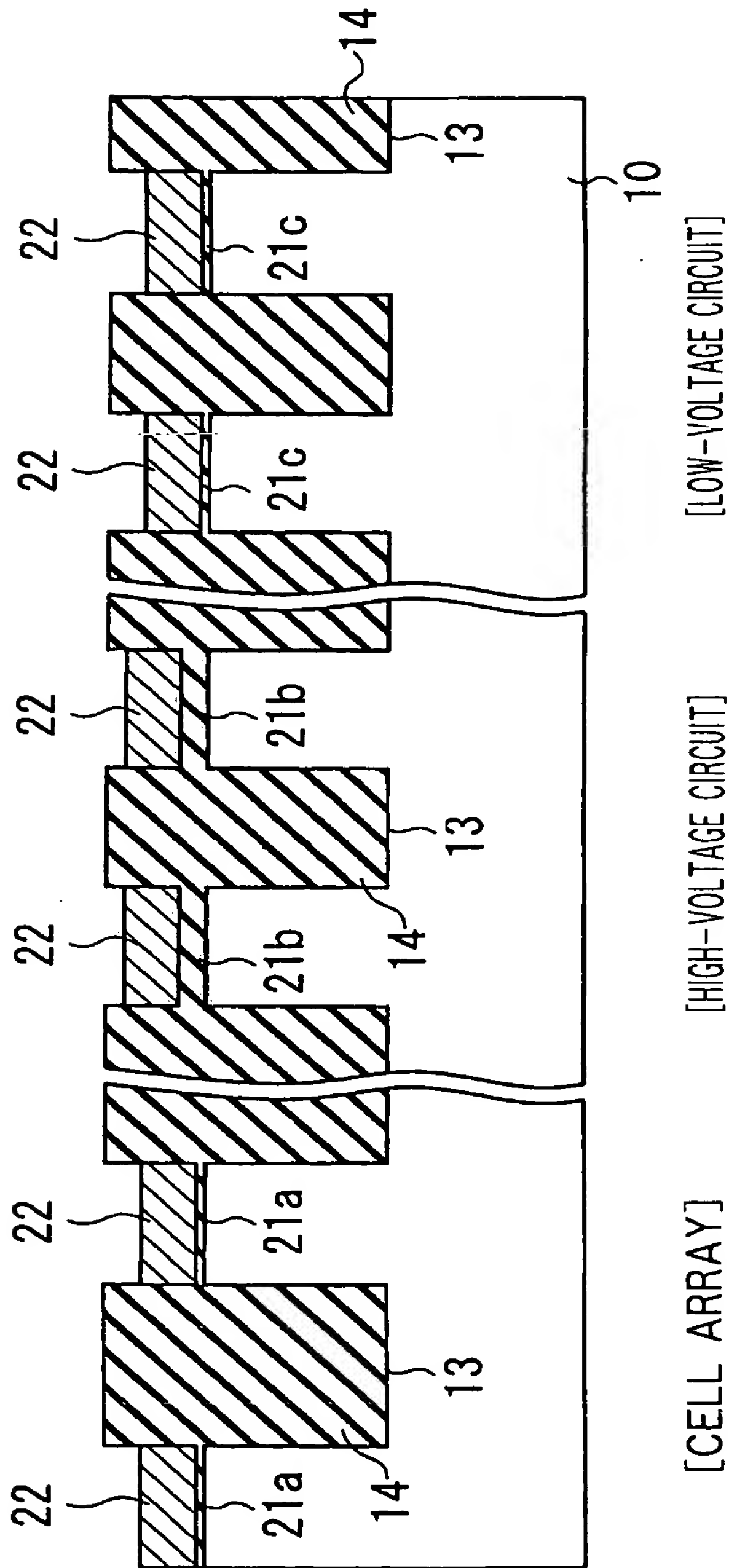


FIG. 23

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ION-IMPLANTATION

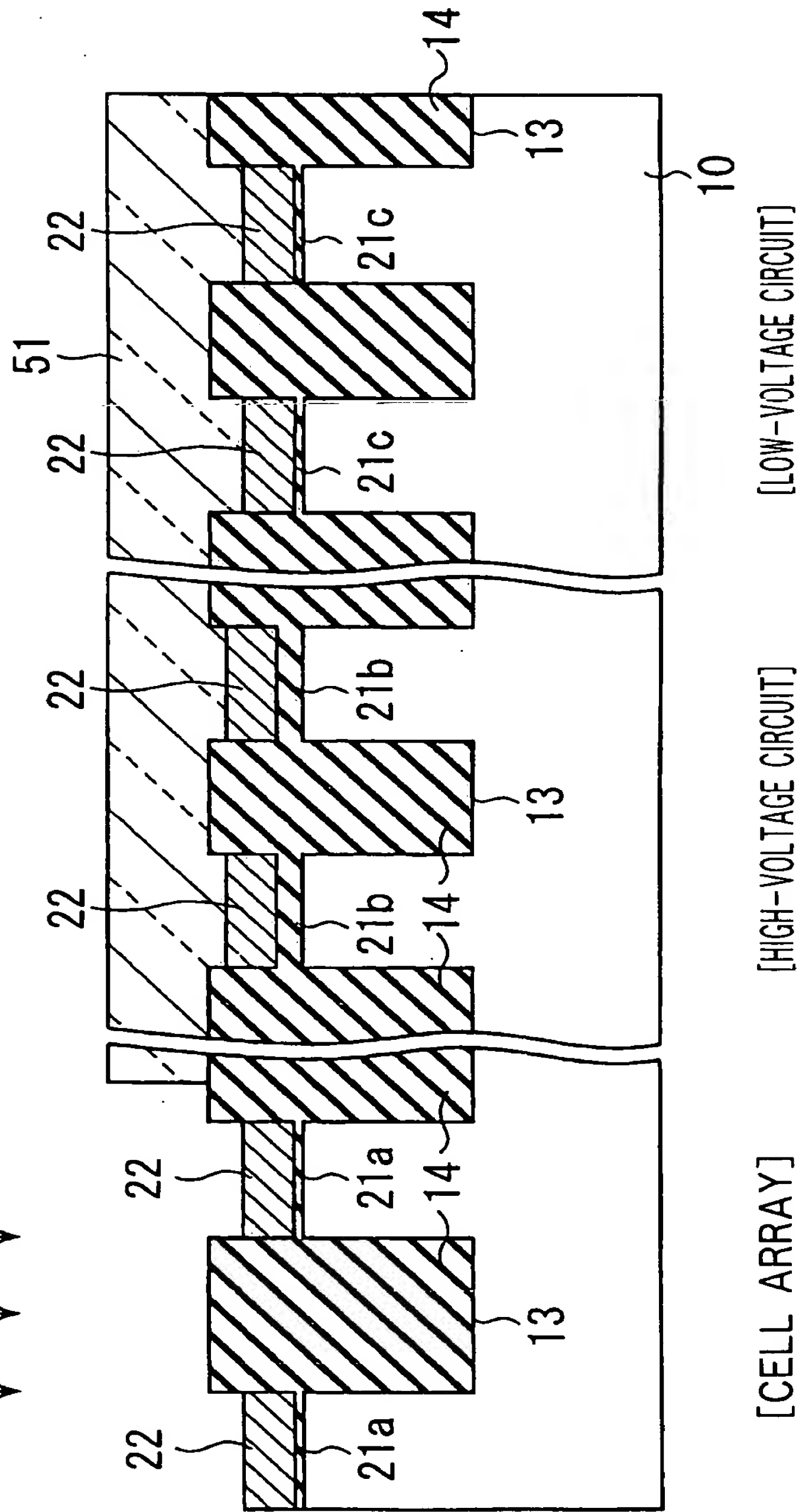


FIG. 24

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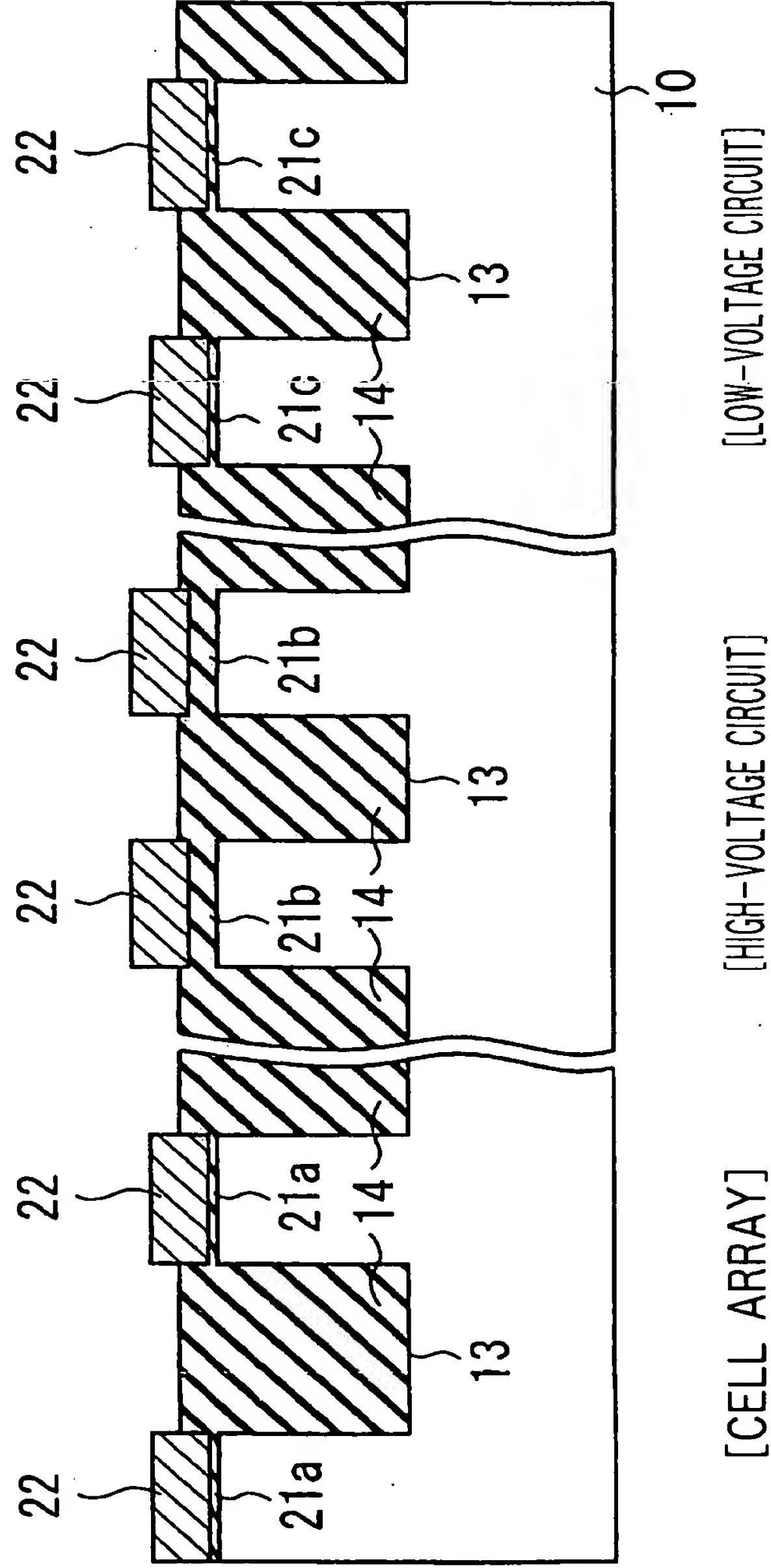


FIG. 25

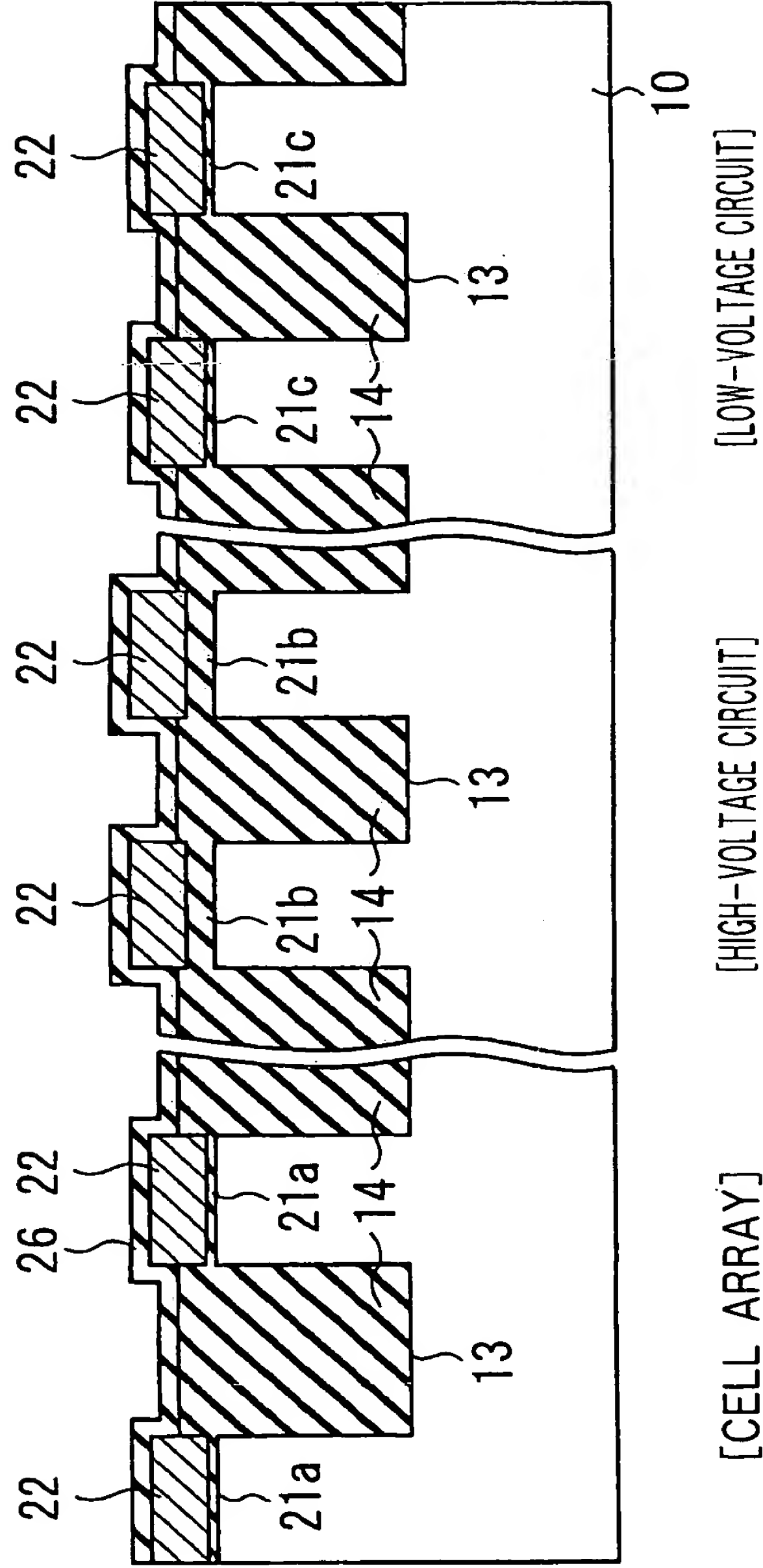


FIG. 26

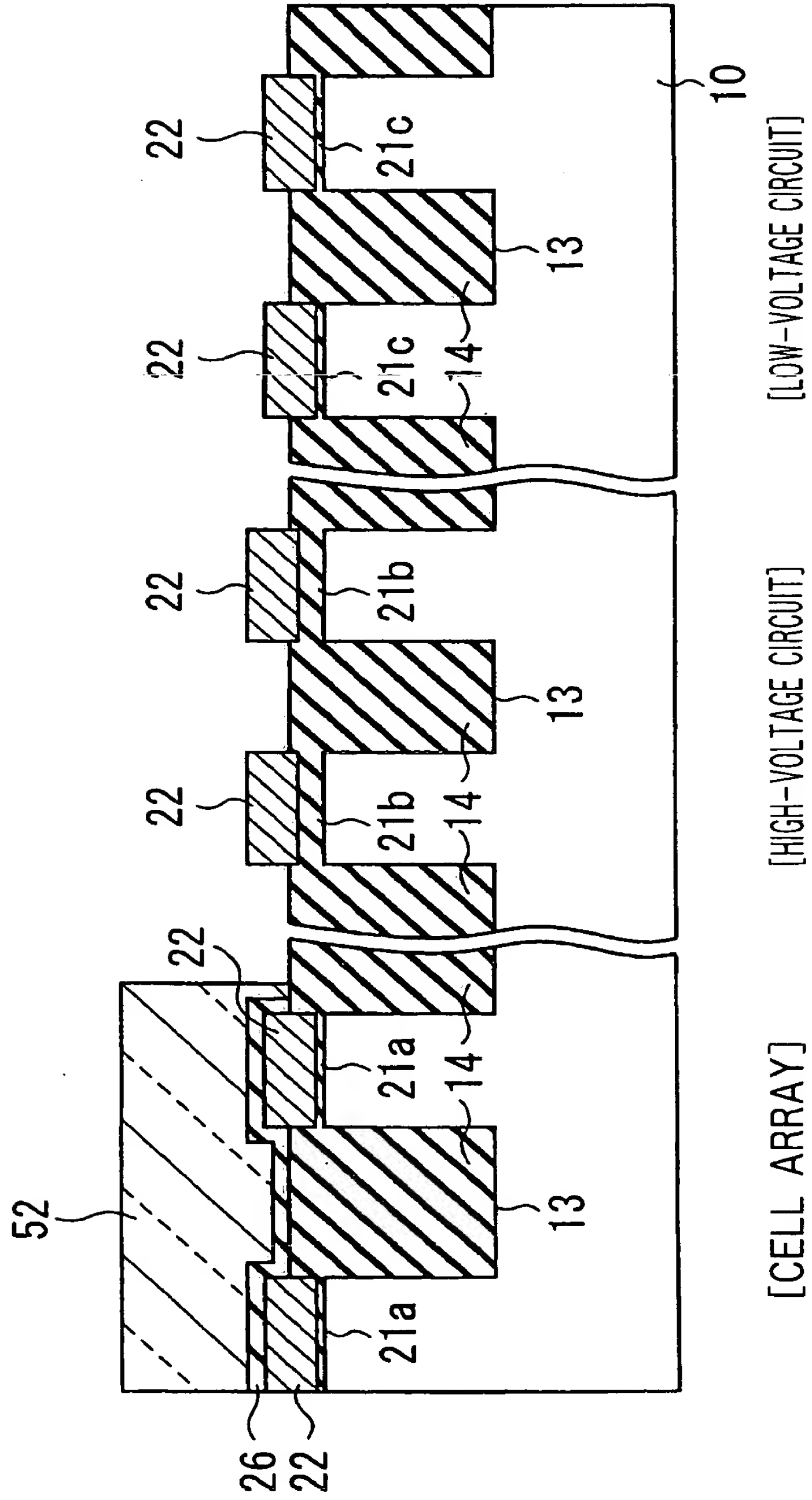


FIG. 27

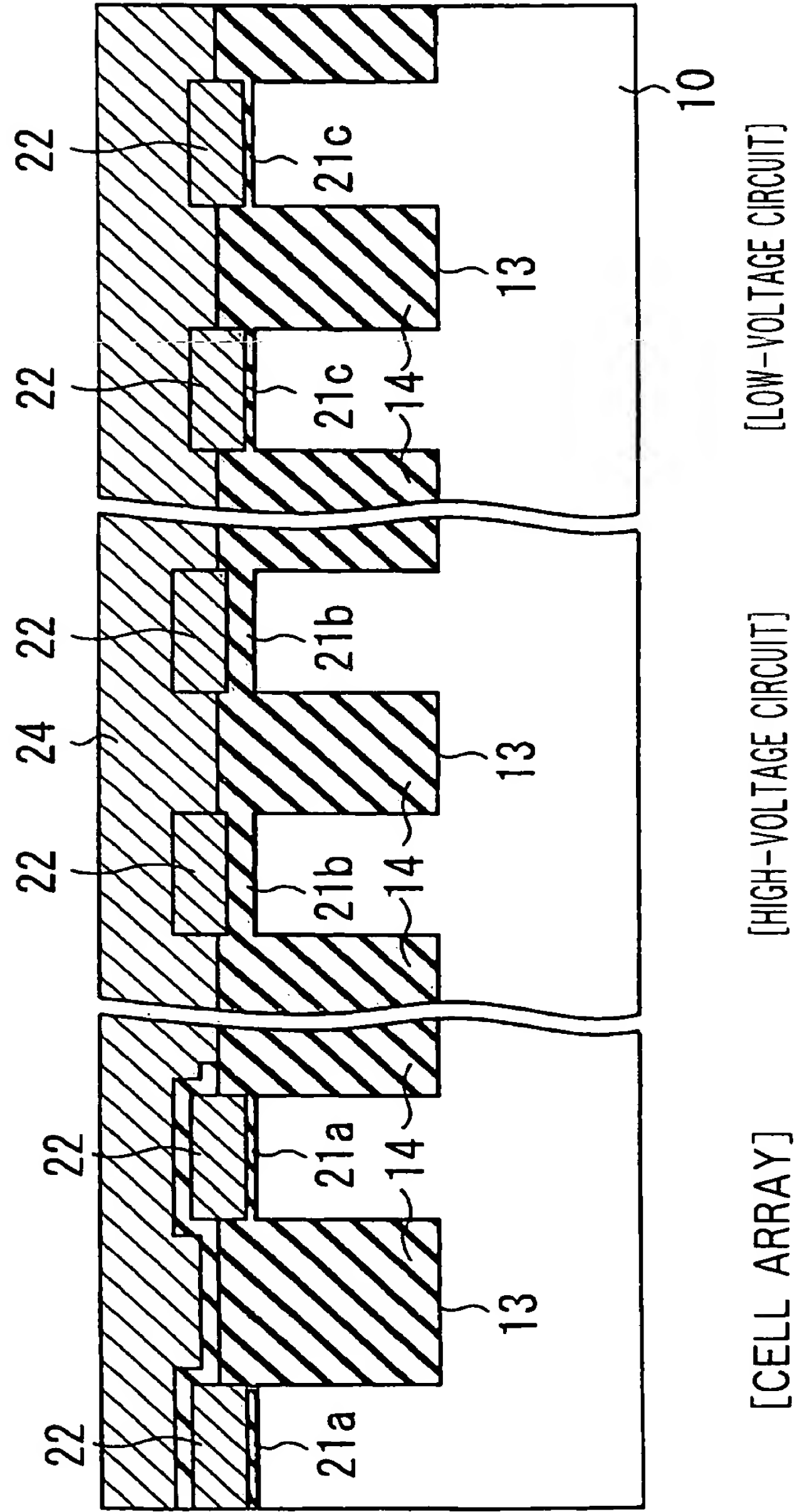


FIG. 28

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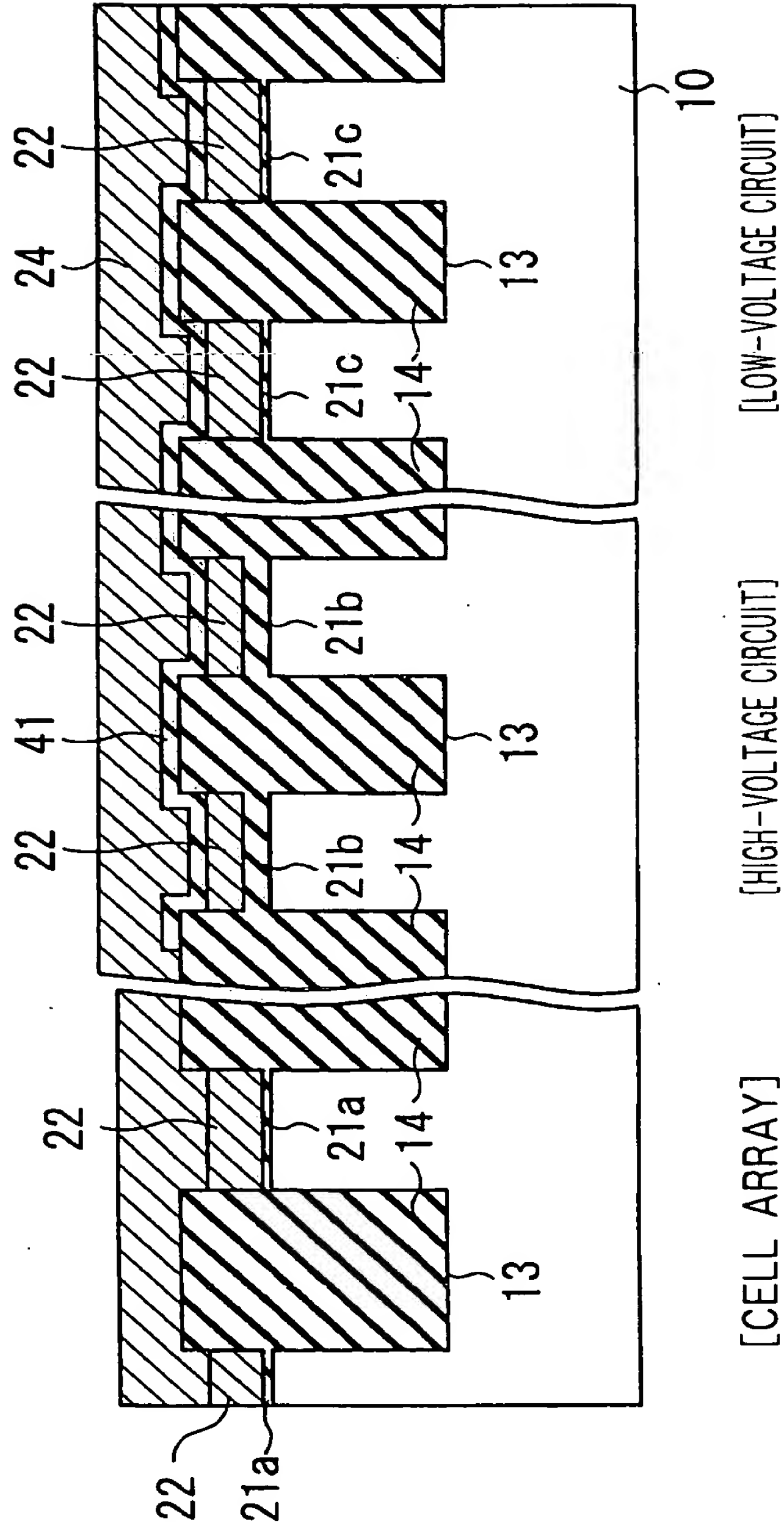


FIG. 29

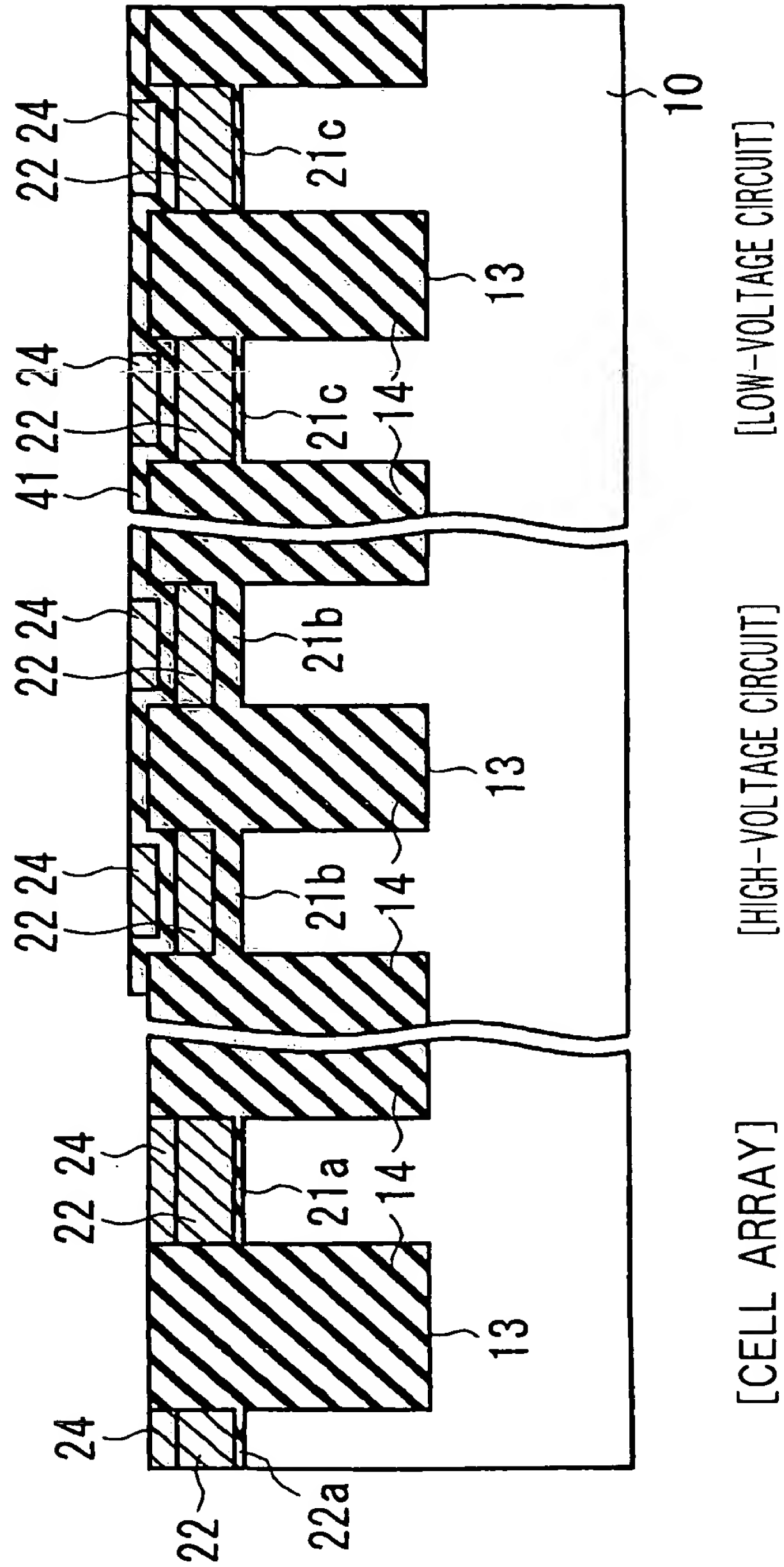


FIG. 30

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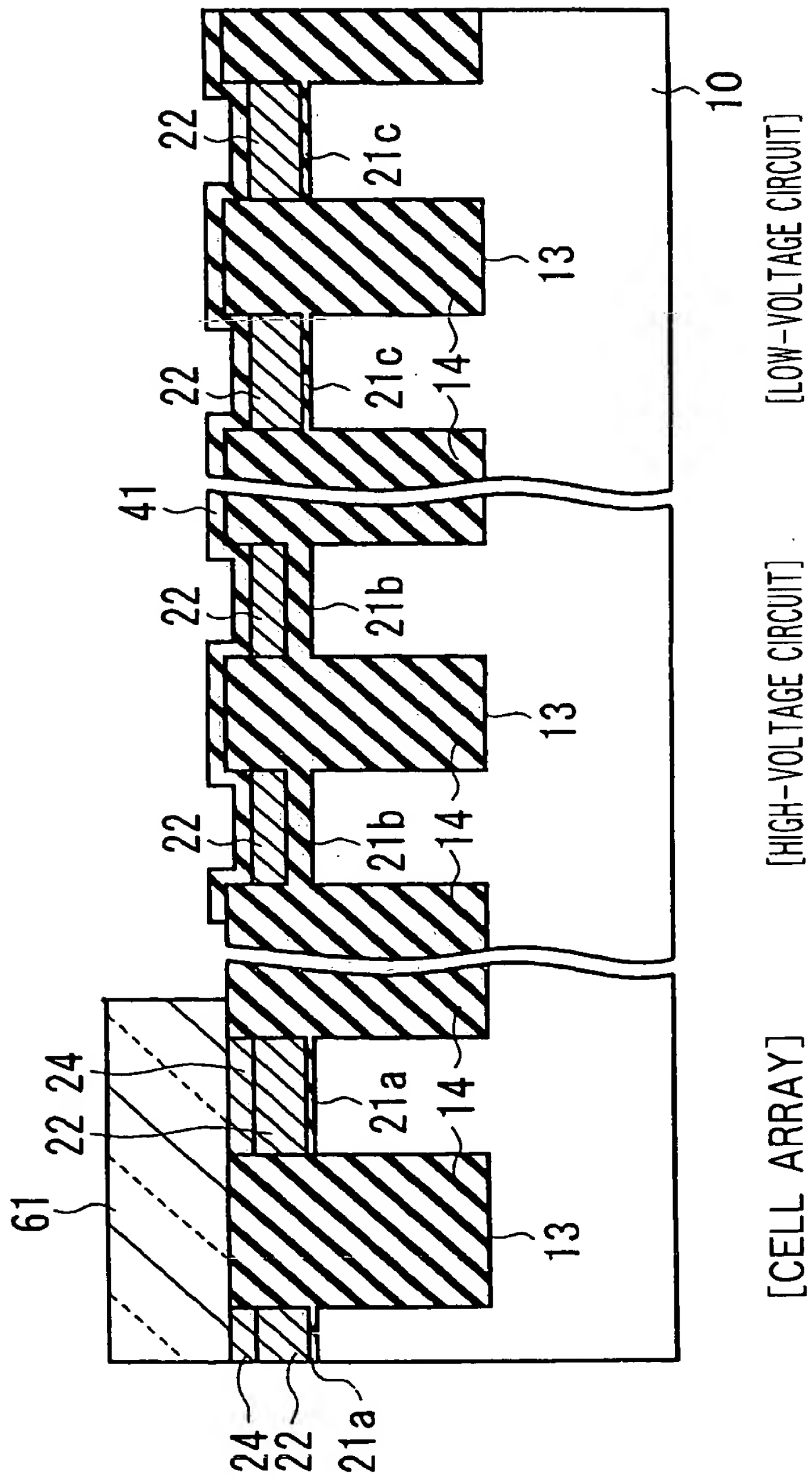


FIG. 31

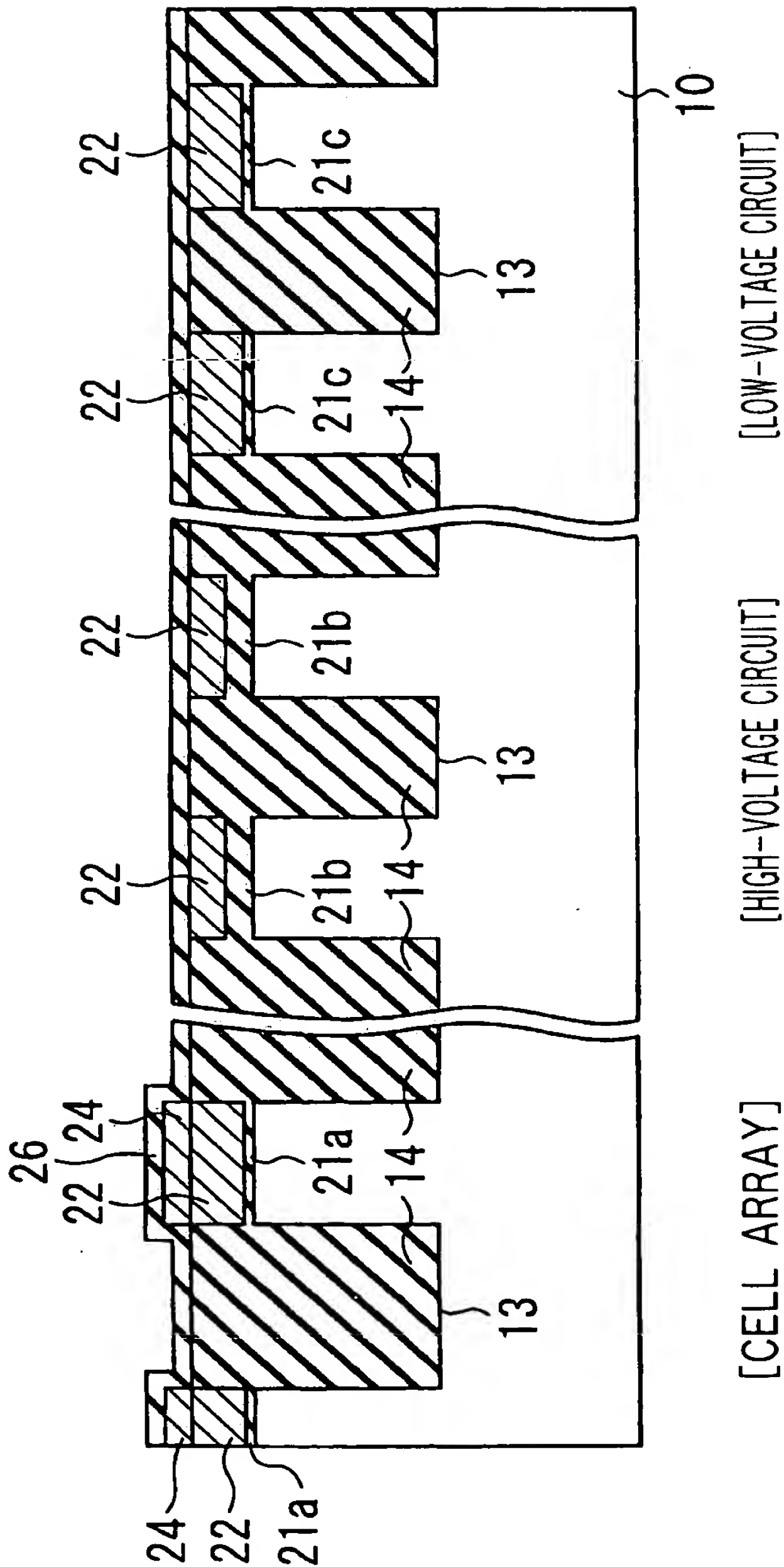


FIG. 32

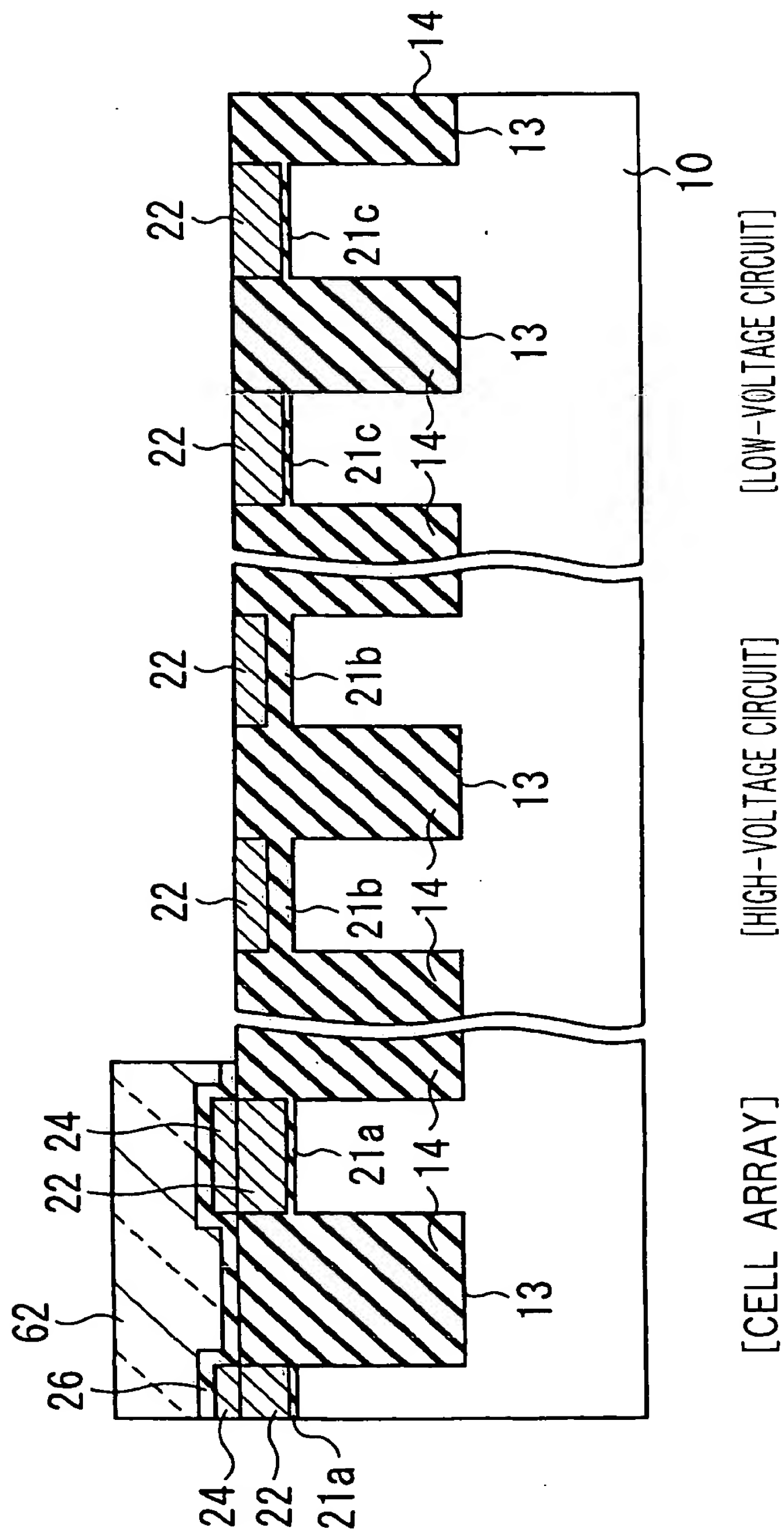


FIG. 33

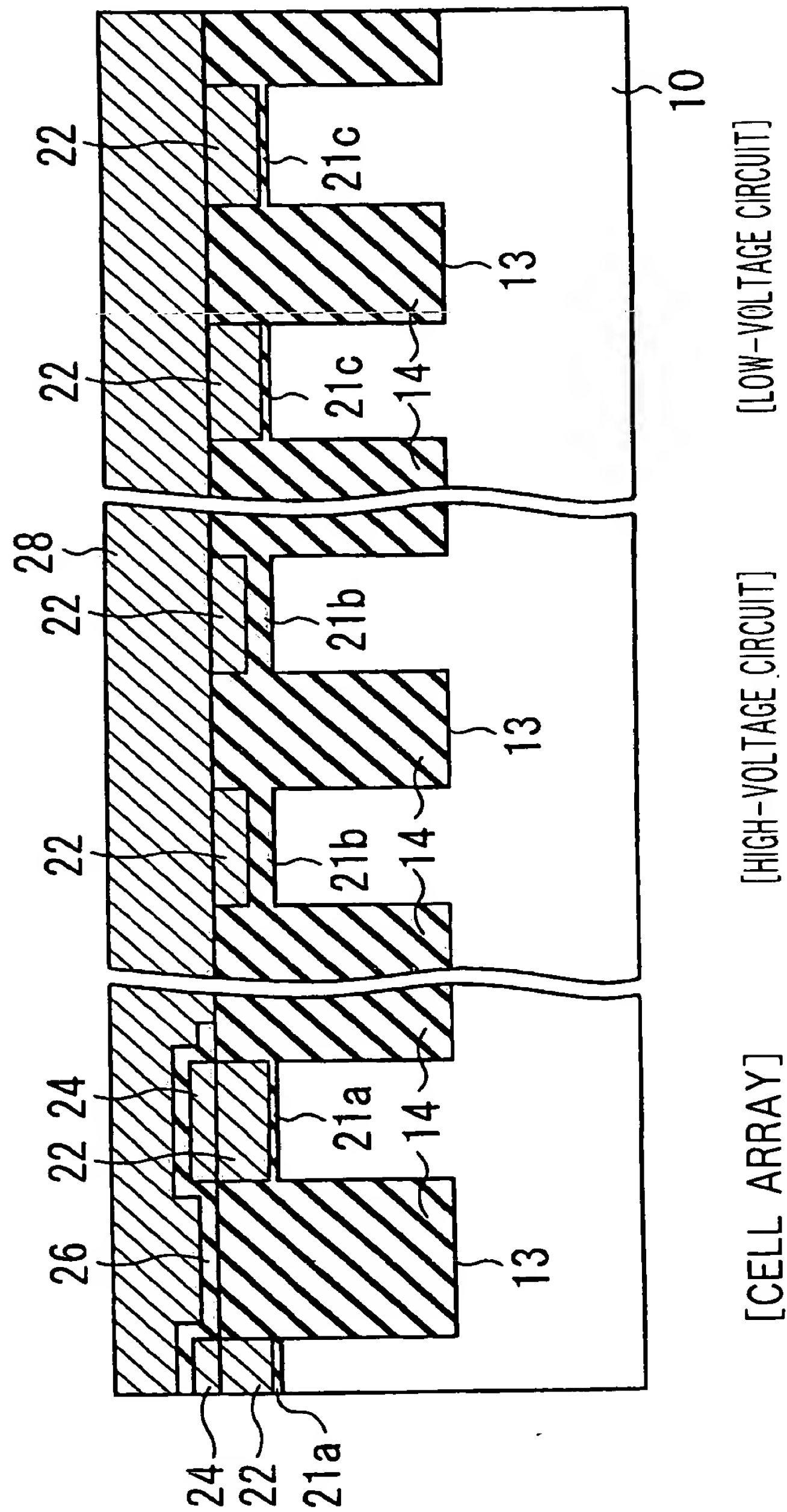


FIG. 34

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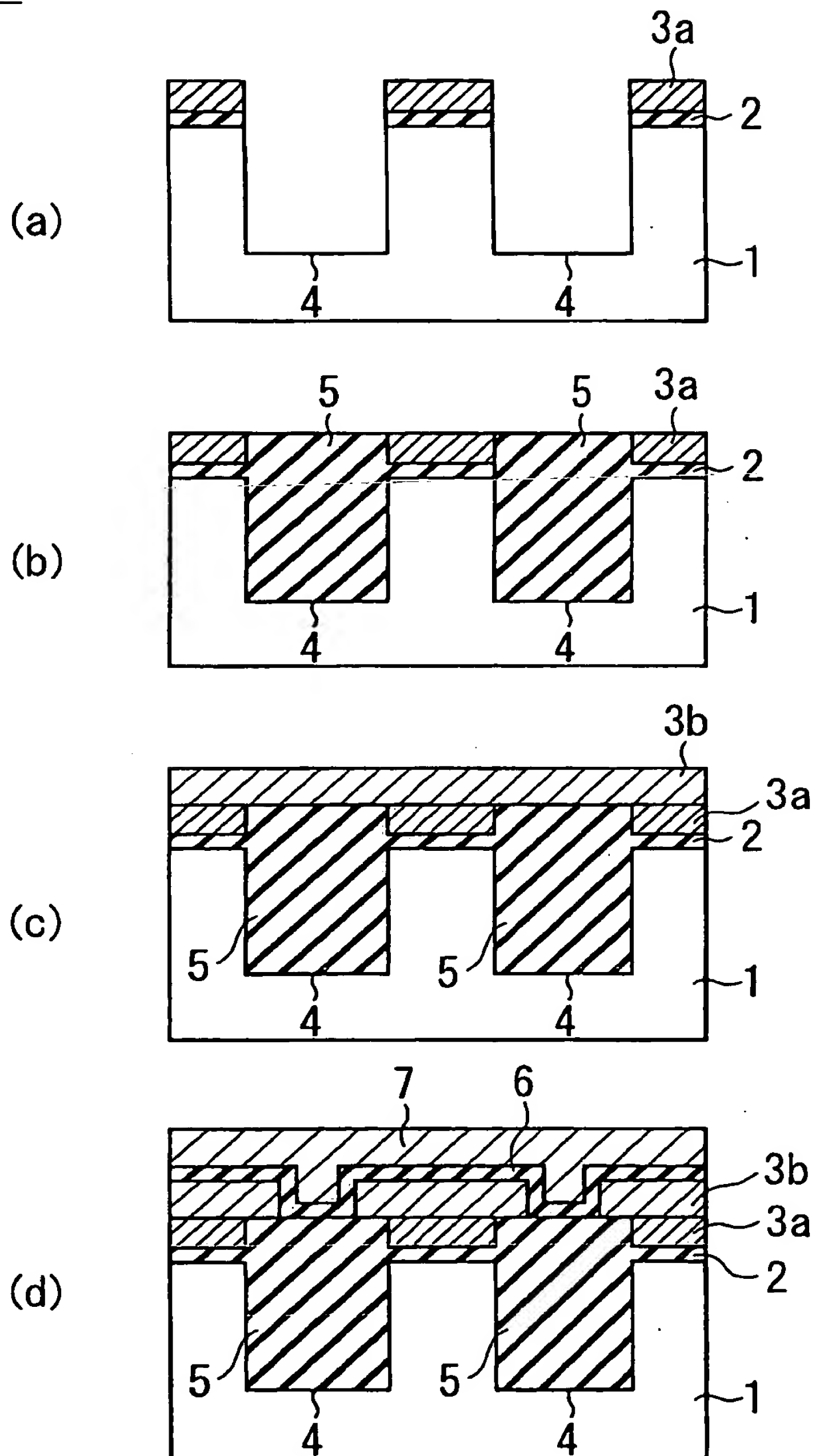


FIG. 35

TOP SECRET

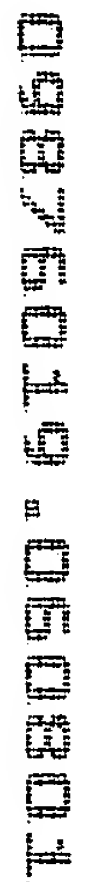


FIG. 36